

1 / 27

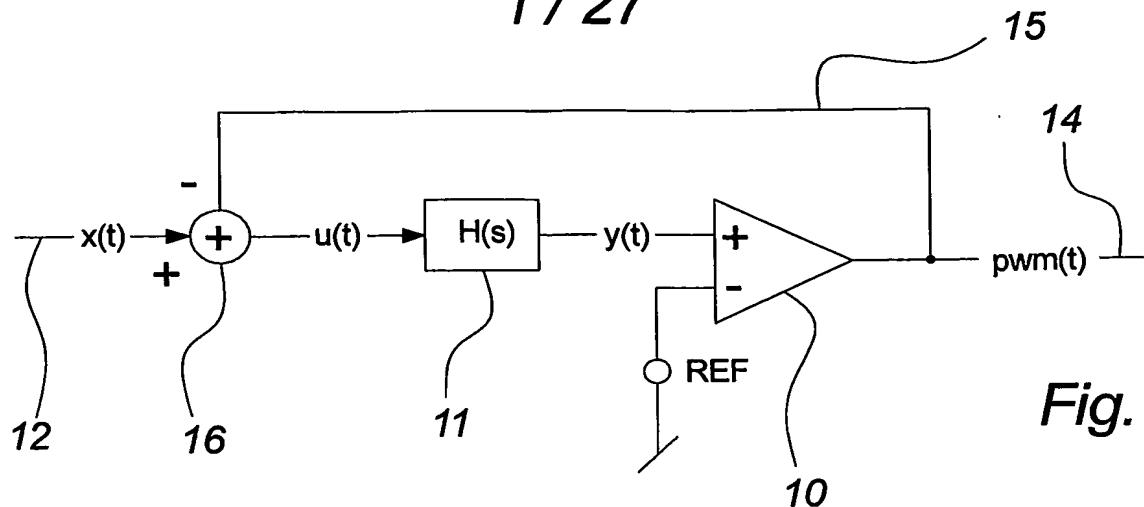


Fig. 1

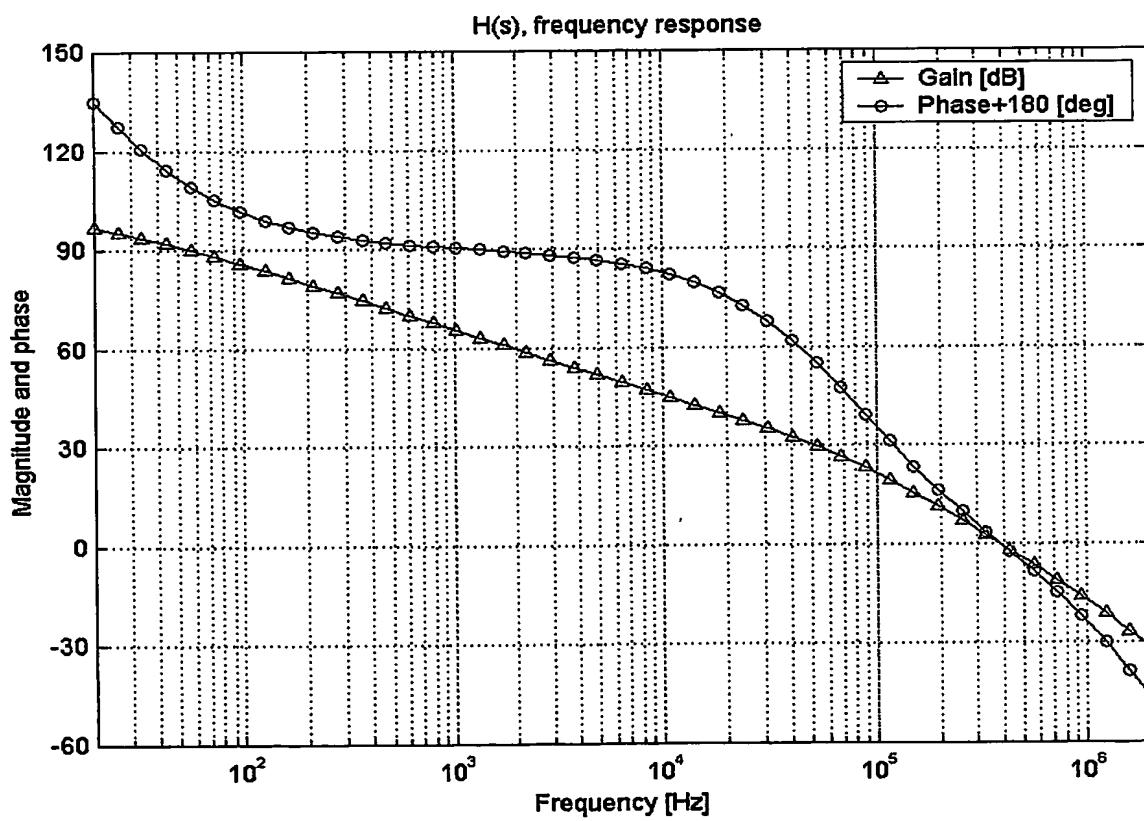


Fig. 2

2 / 27

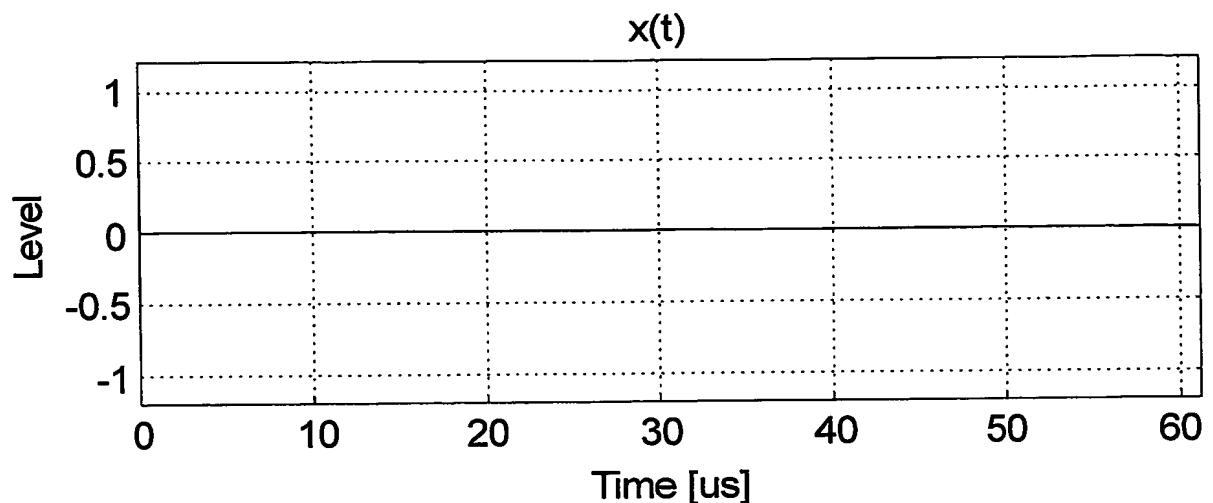


Fig. 3a

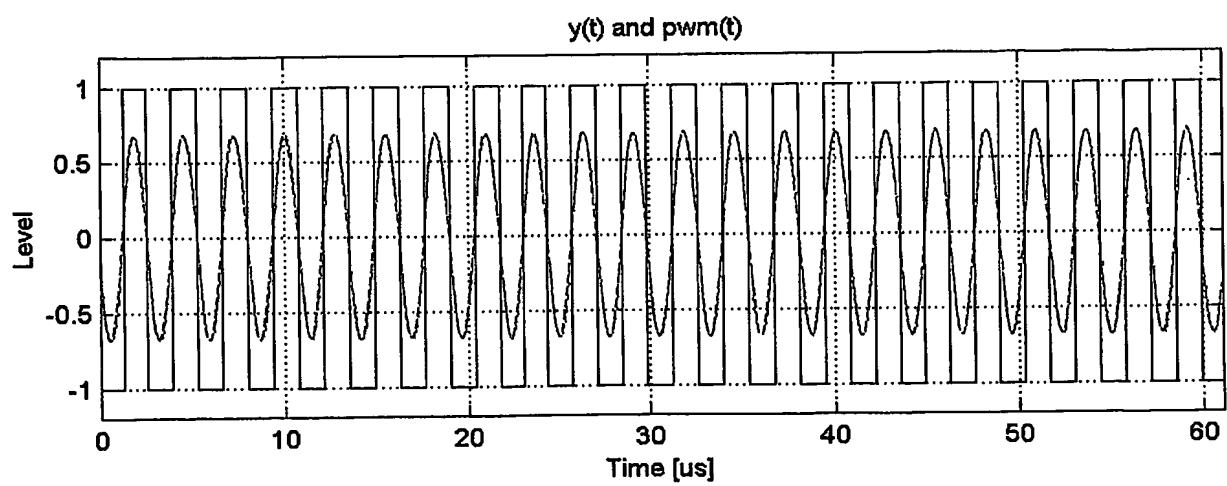


Fig. 3b

3 / 27

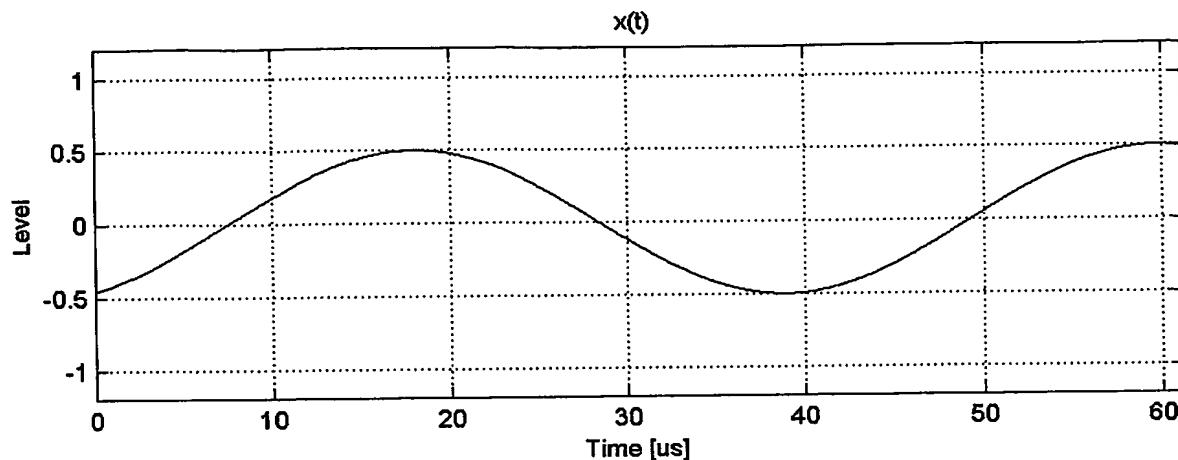


Fig. 4a

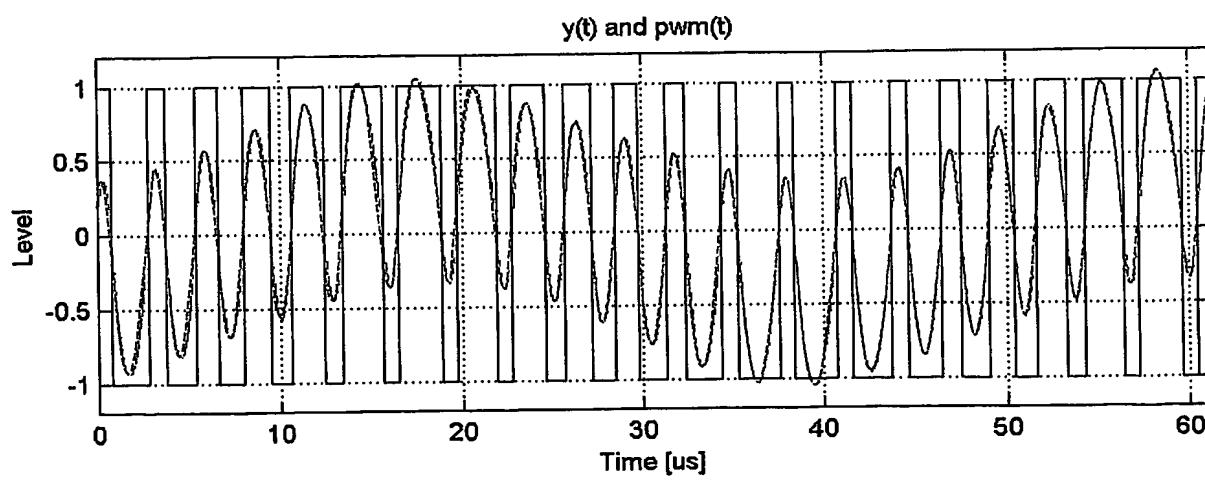


Fig. 4b

4 / 27

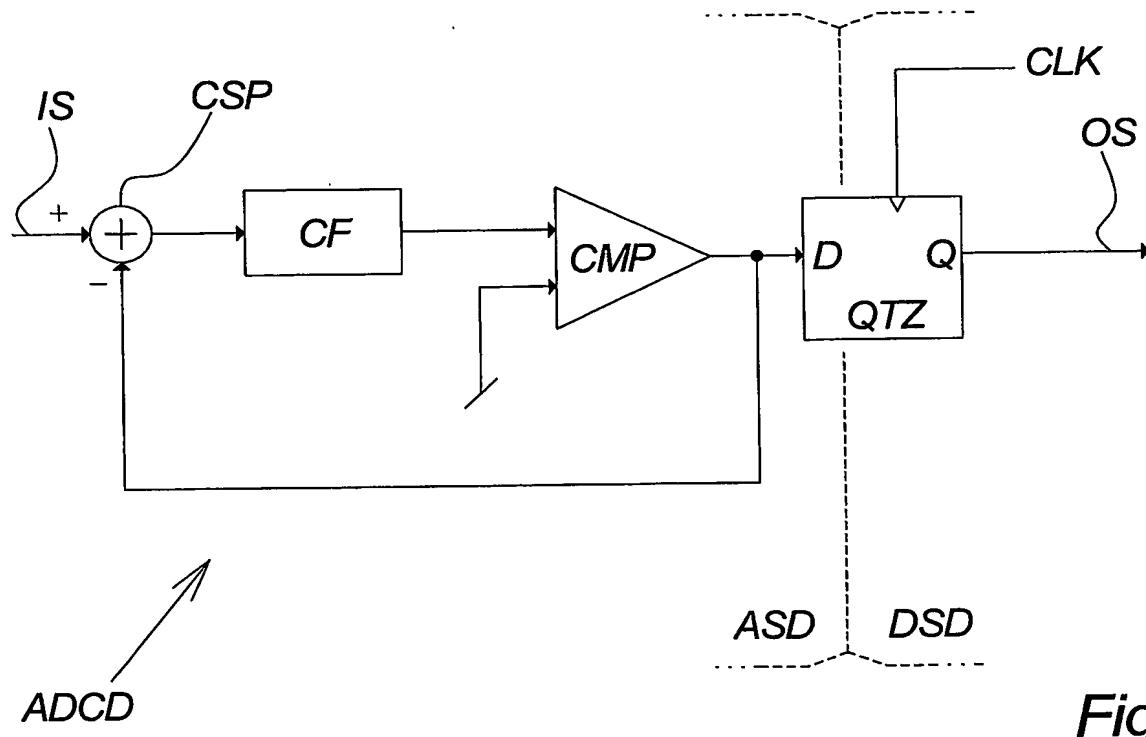


Fig. 5

5 / 27

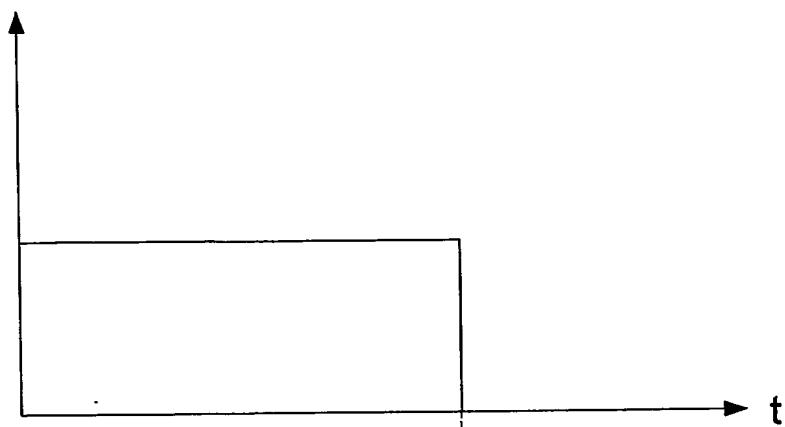


Fig. 6a

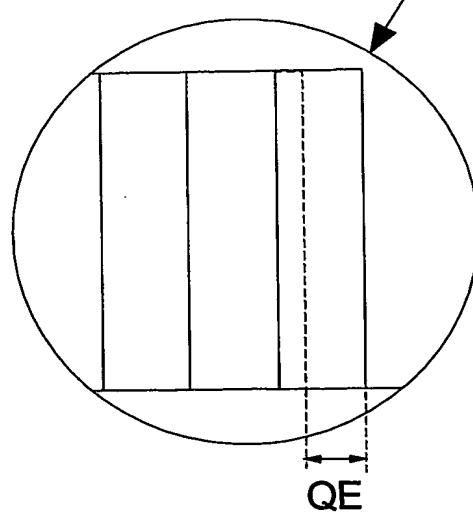
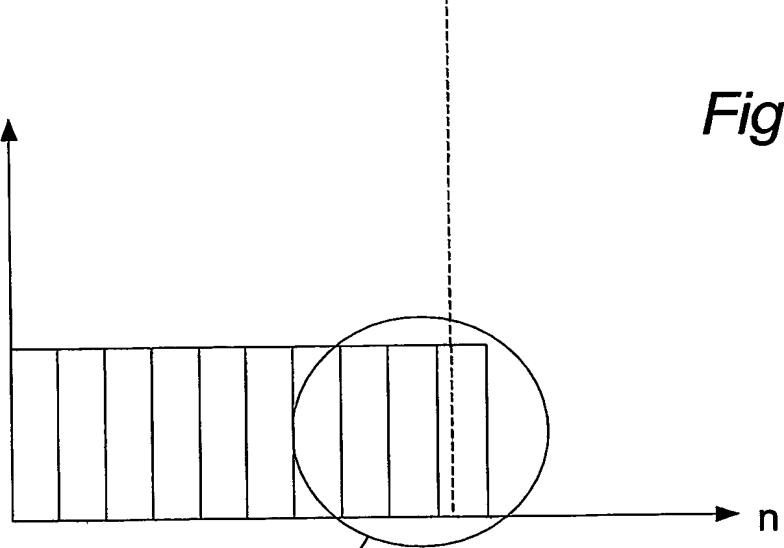


Fig. 6b

6/27

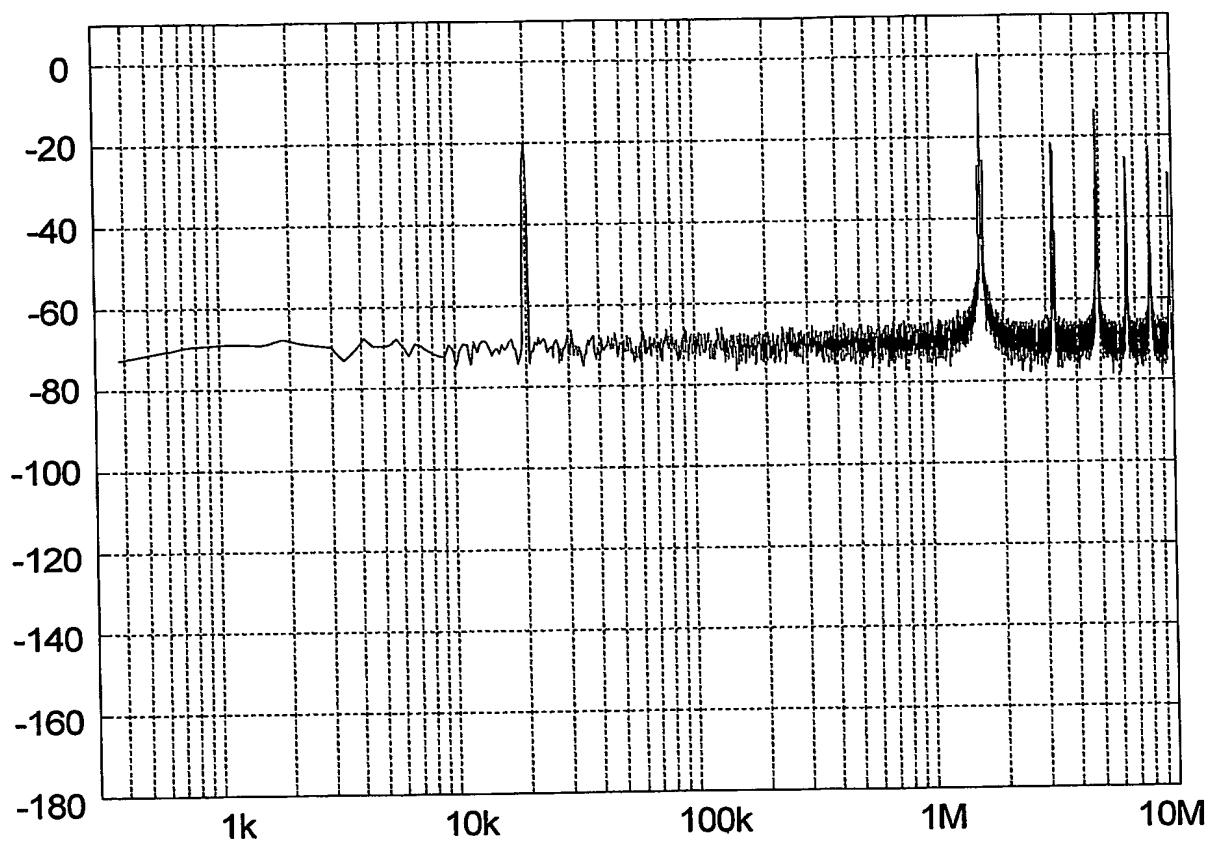


Fig. 7

7 / 27

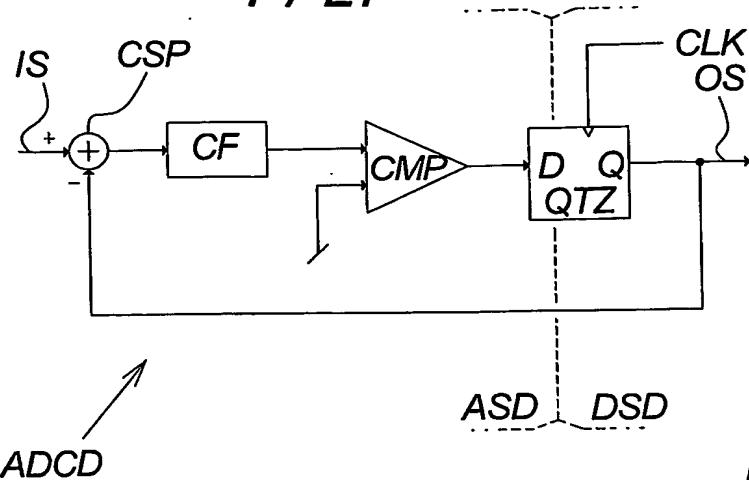


Fig. 8a

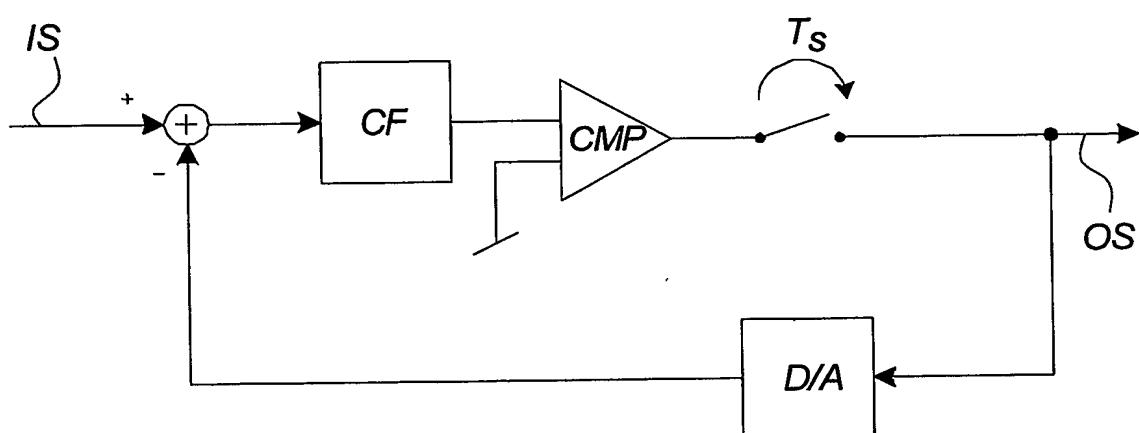
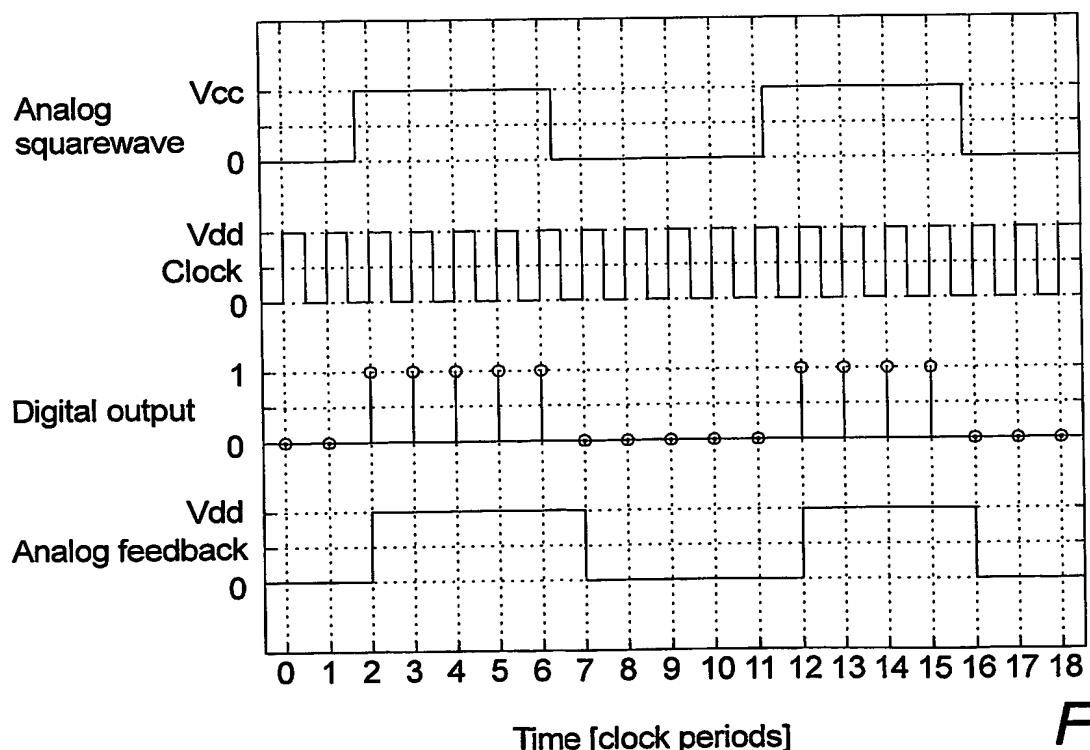


Fig. 8b



Time [clock periods]

8 / 27

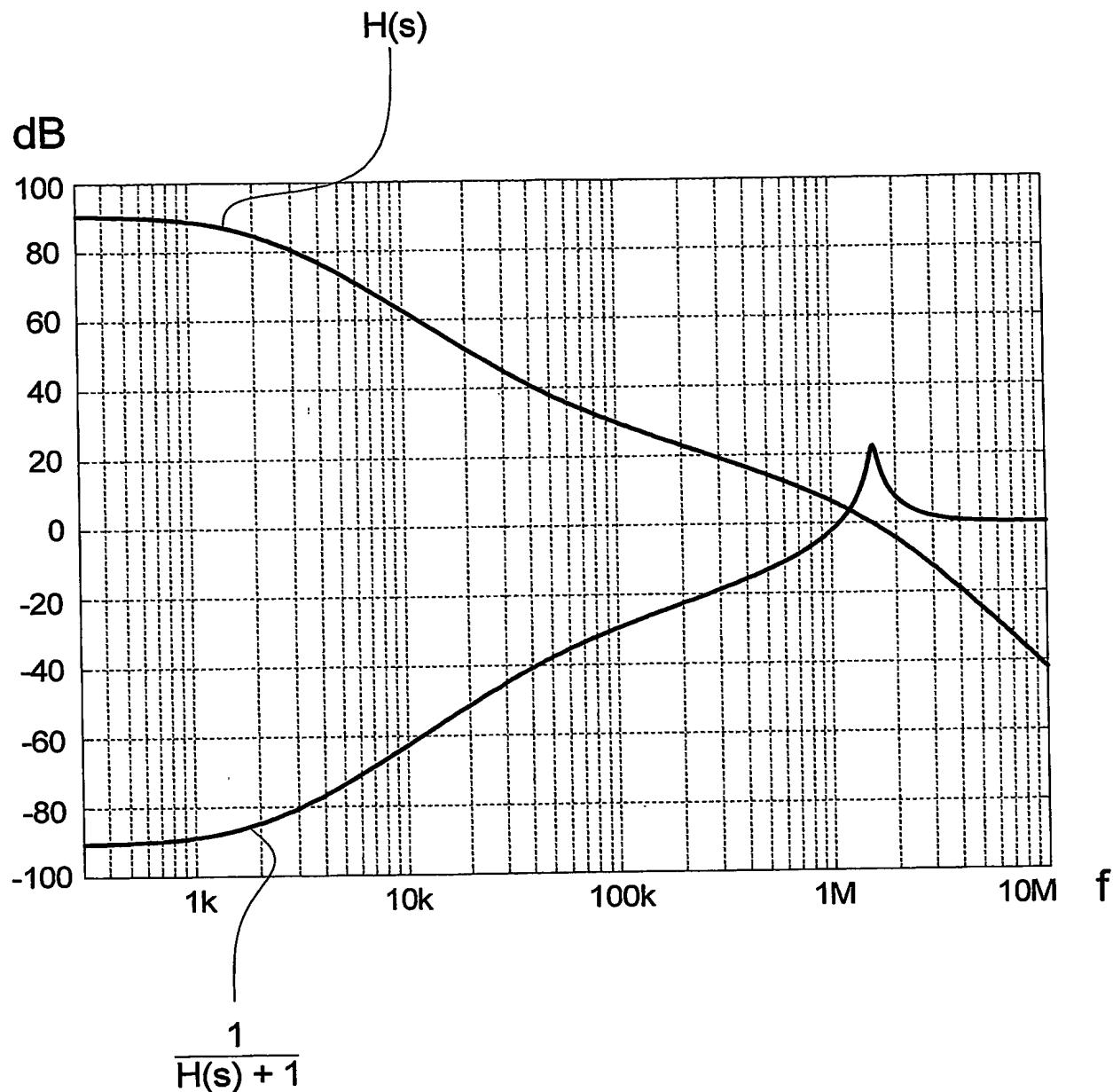


Fig. 9

9 / 27

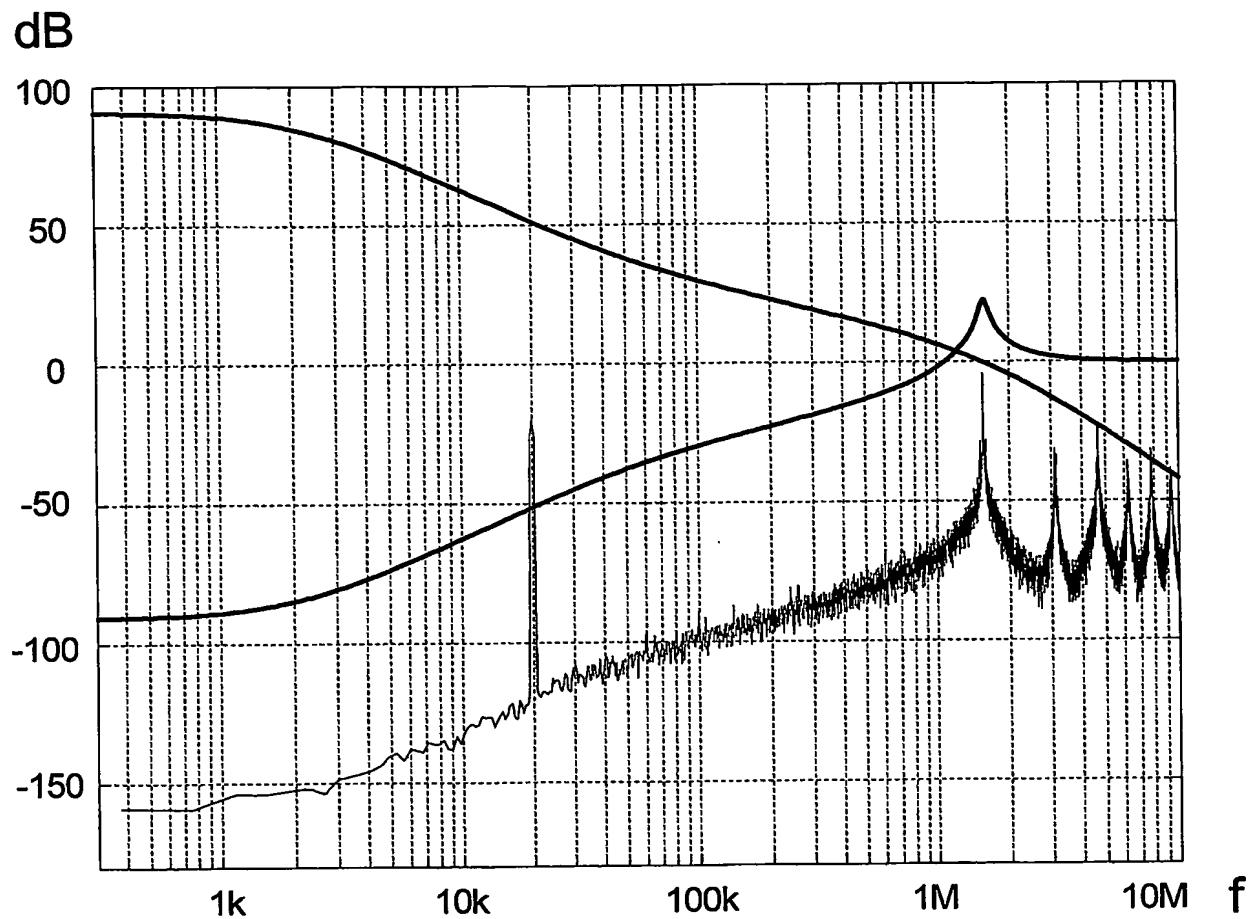


Fig. 10

10 / 27

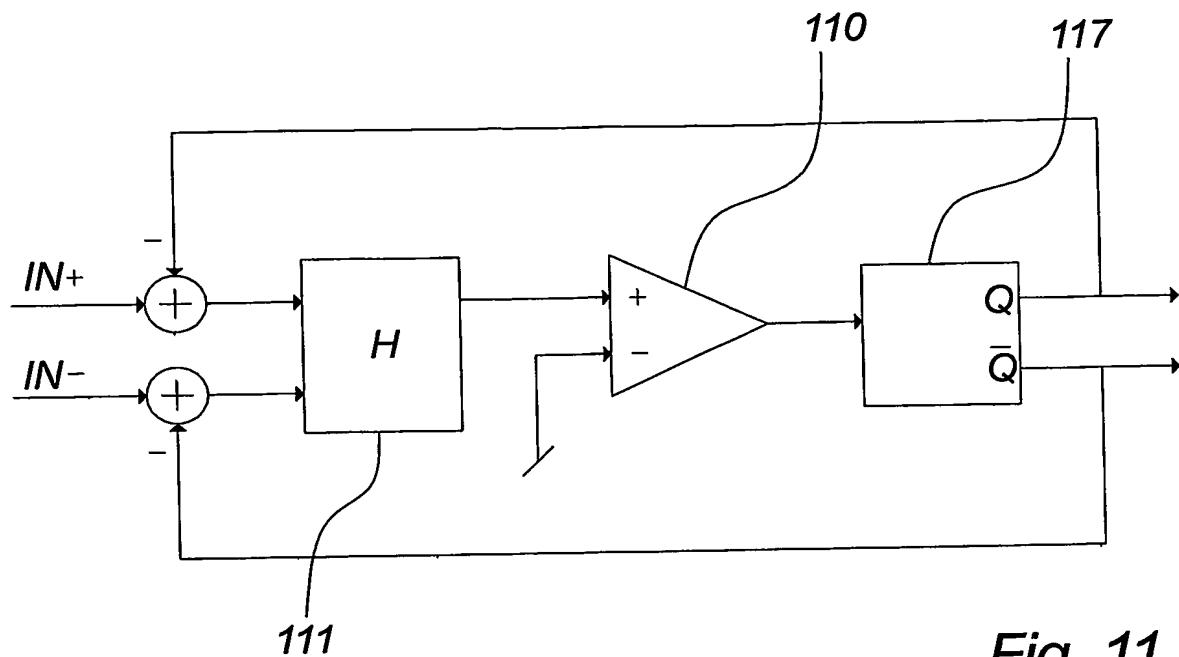


Fig. 11

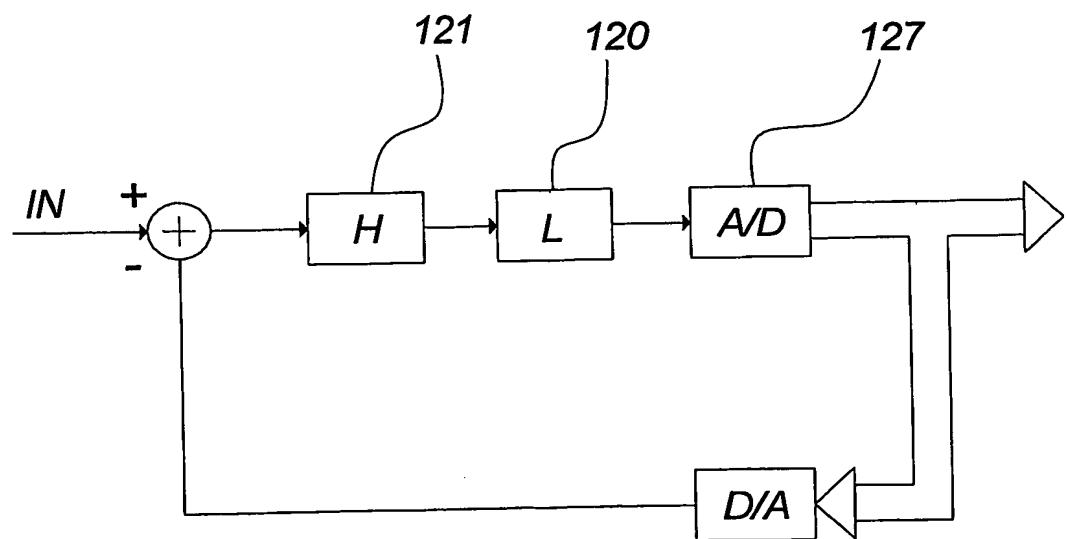


Fig. 12

11 / 27

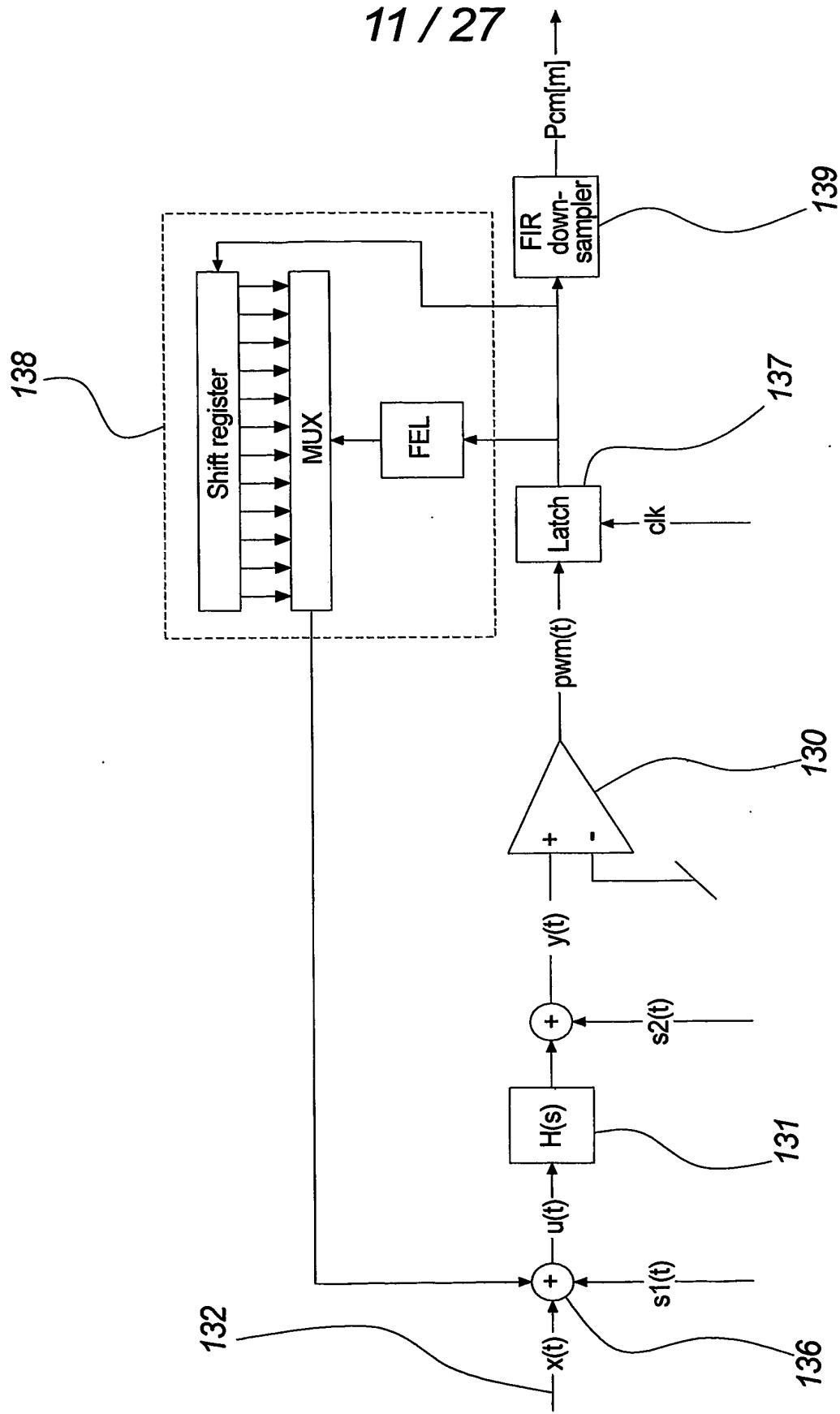
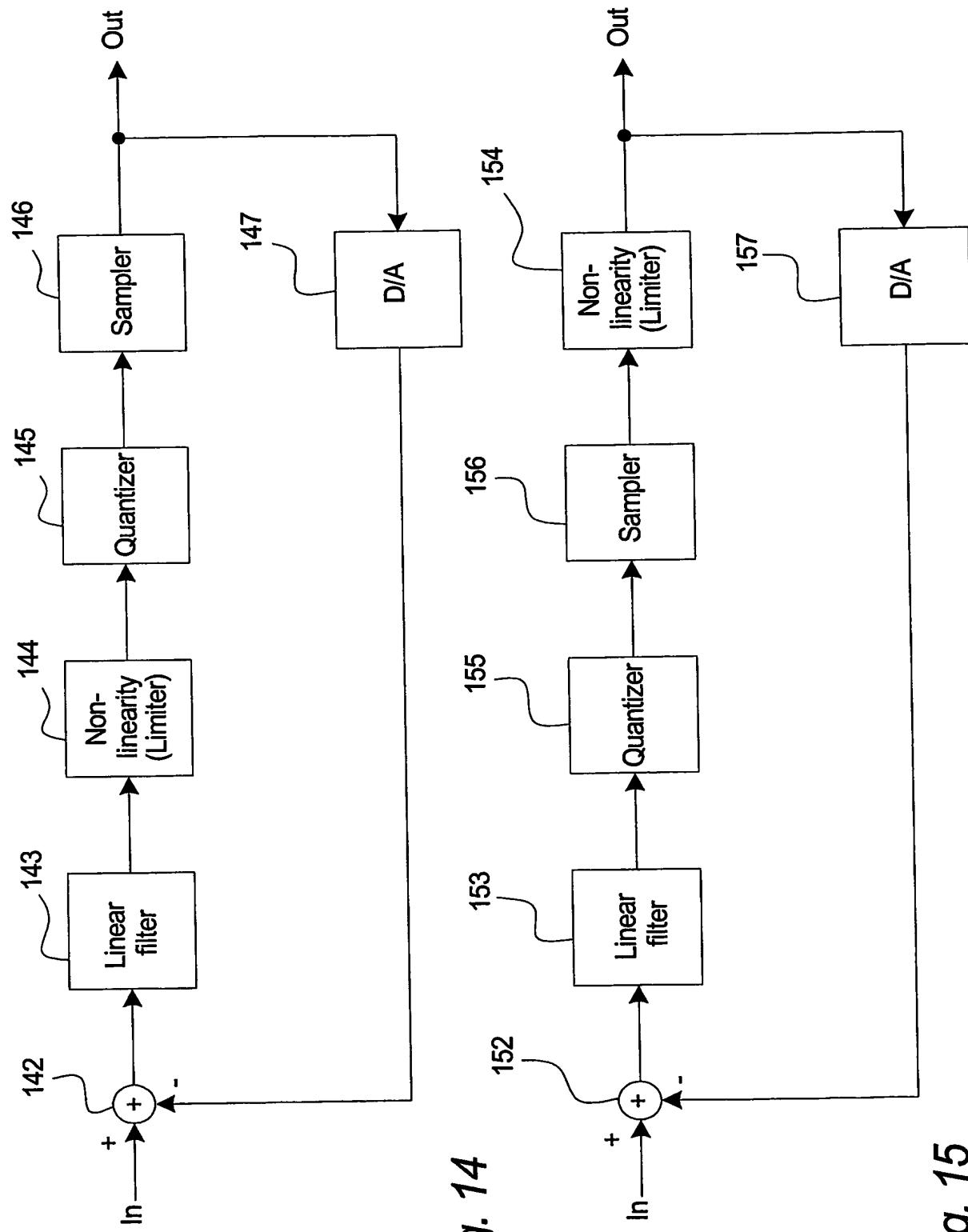


Fig. 13

12 / 27



13 / 27

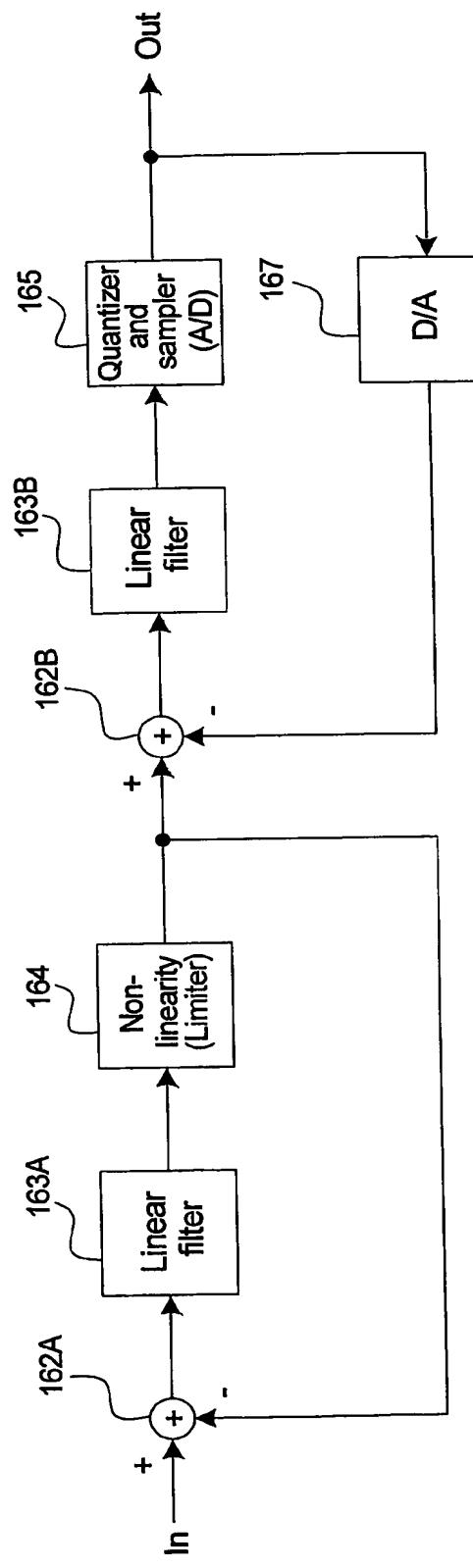


Fig. 16

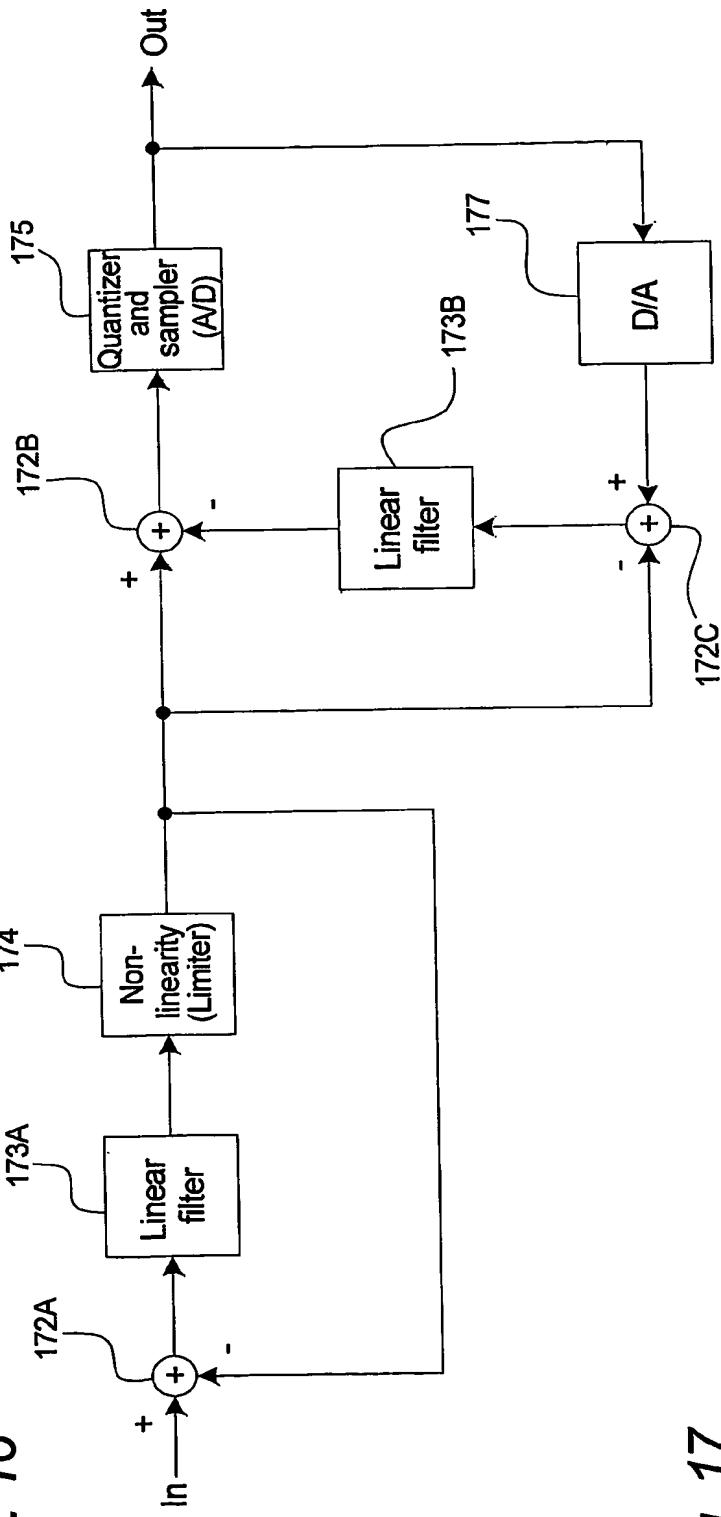


Fig. 17

14 / 27

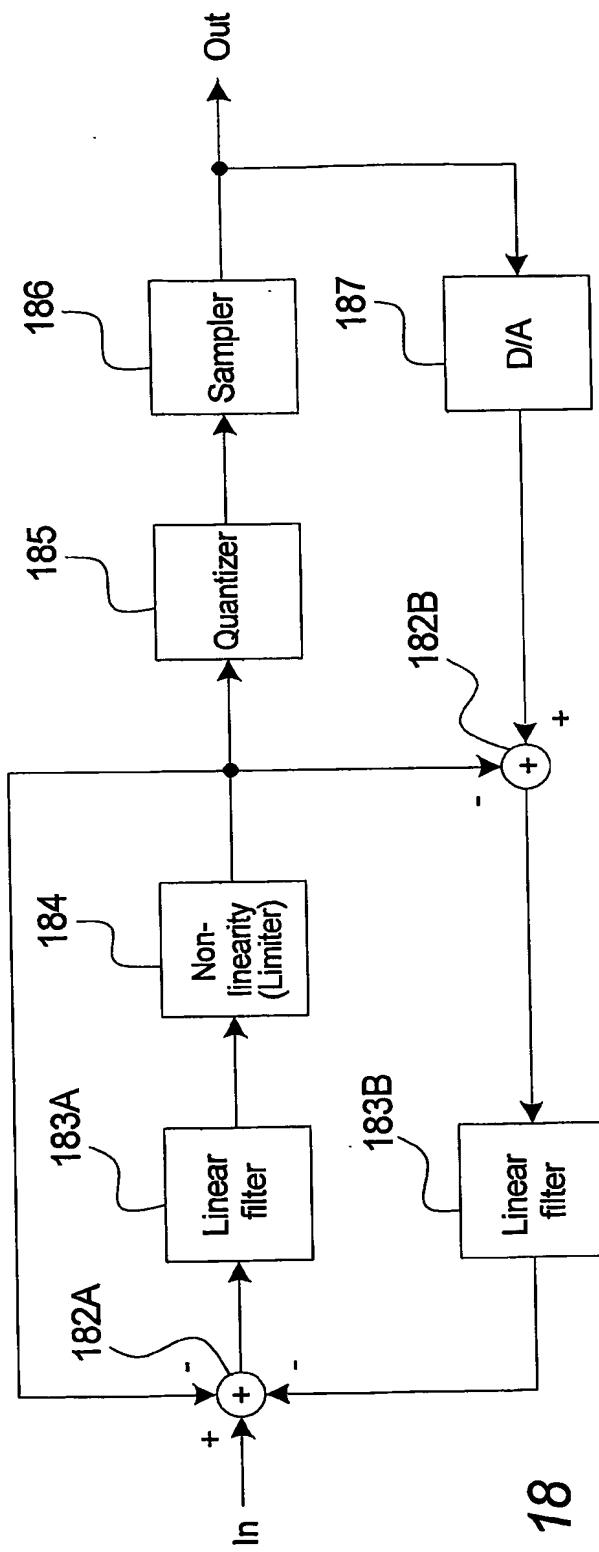


Fig. 18

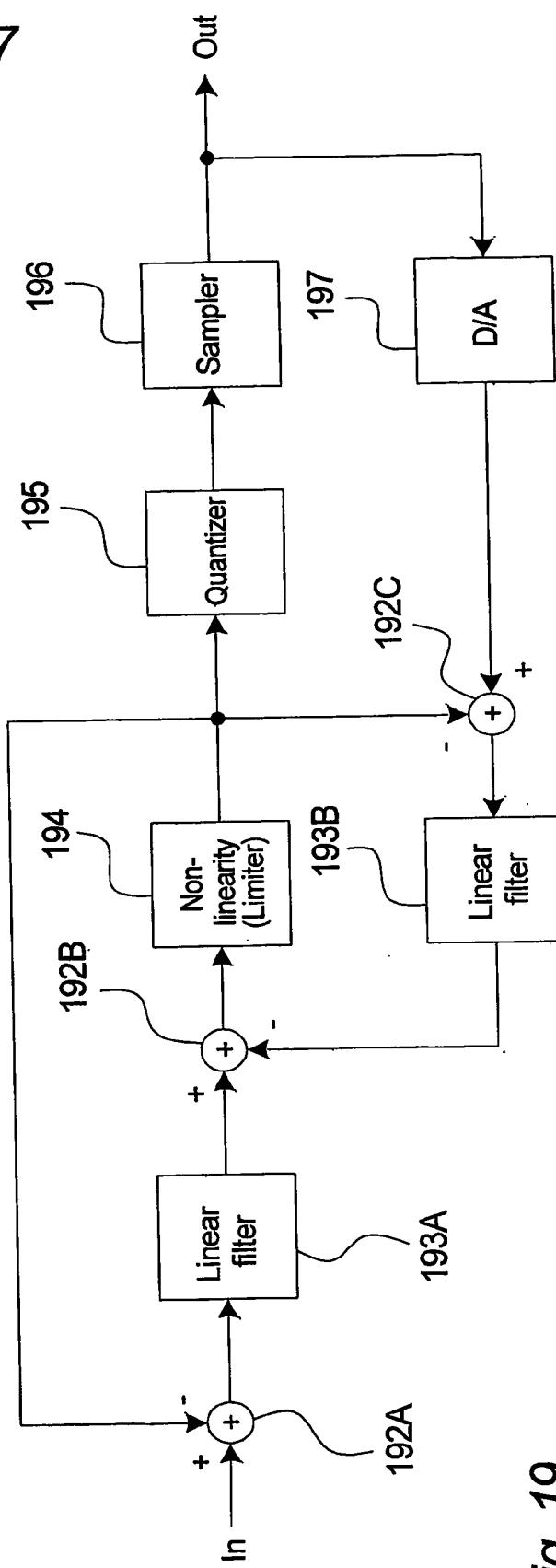


Fig. 19

15 / 27

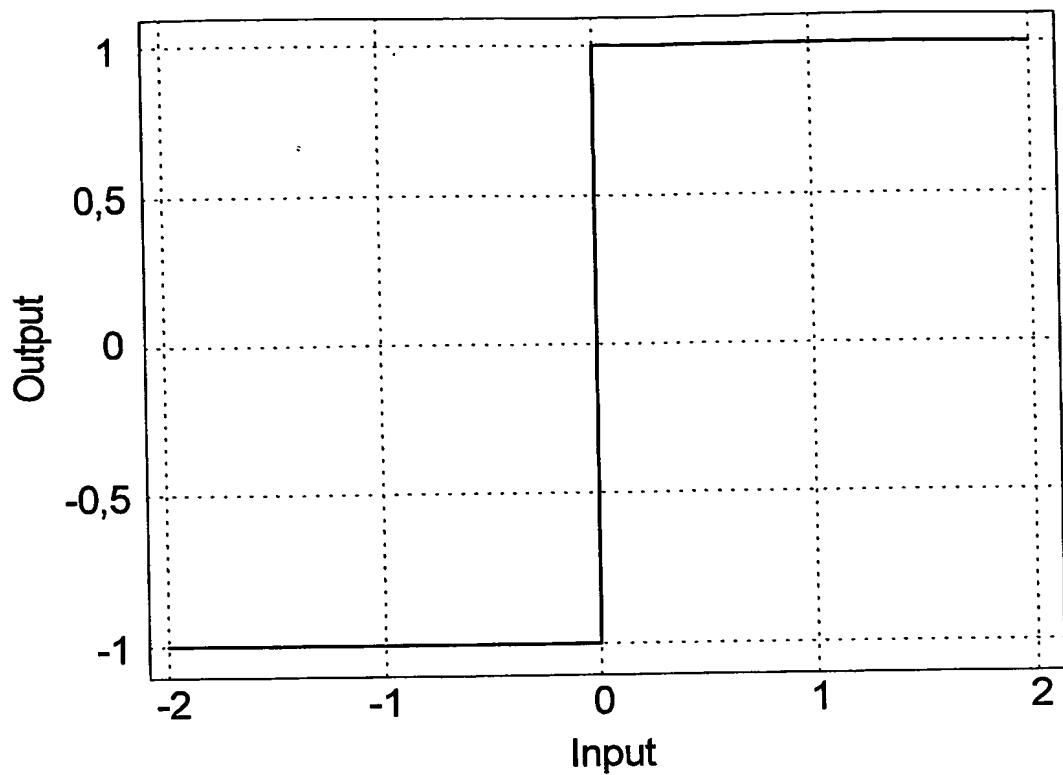


Fig. 20

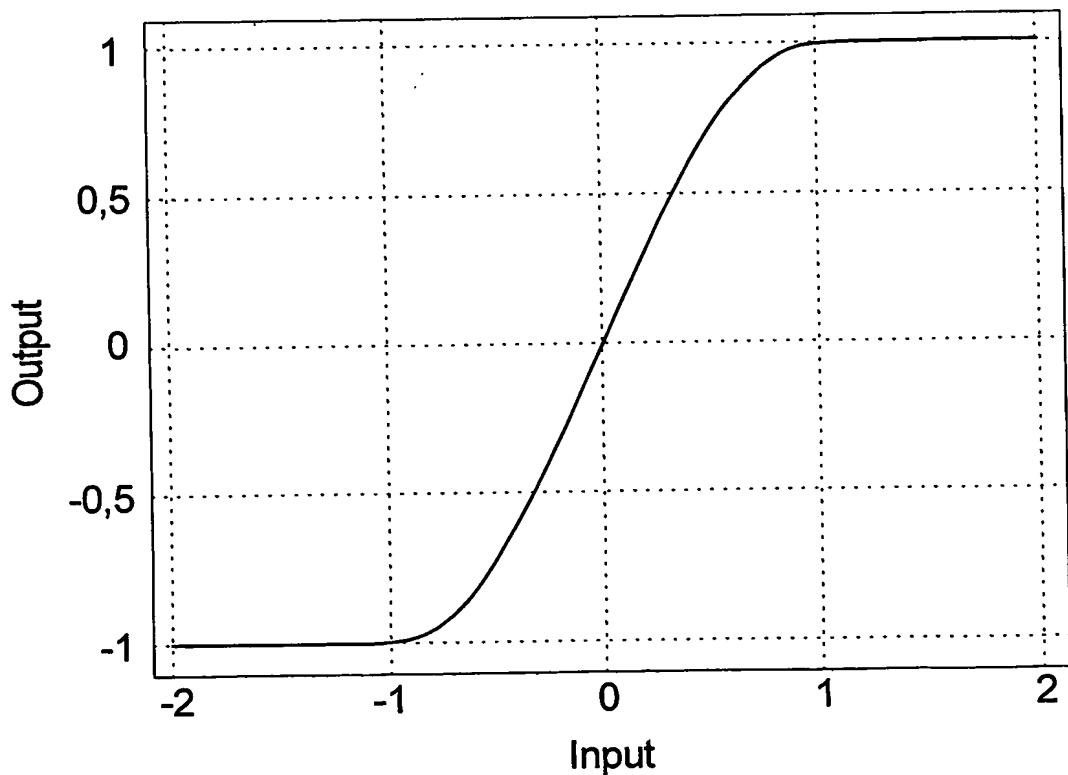


Fig. 21

16 / 27

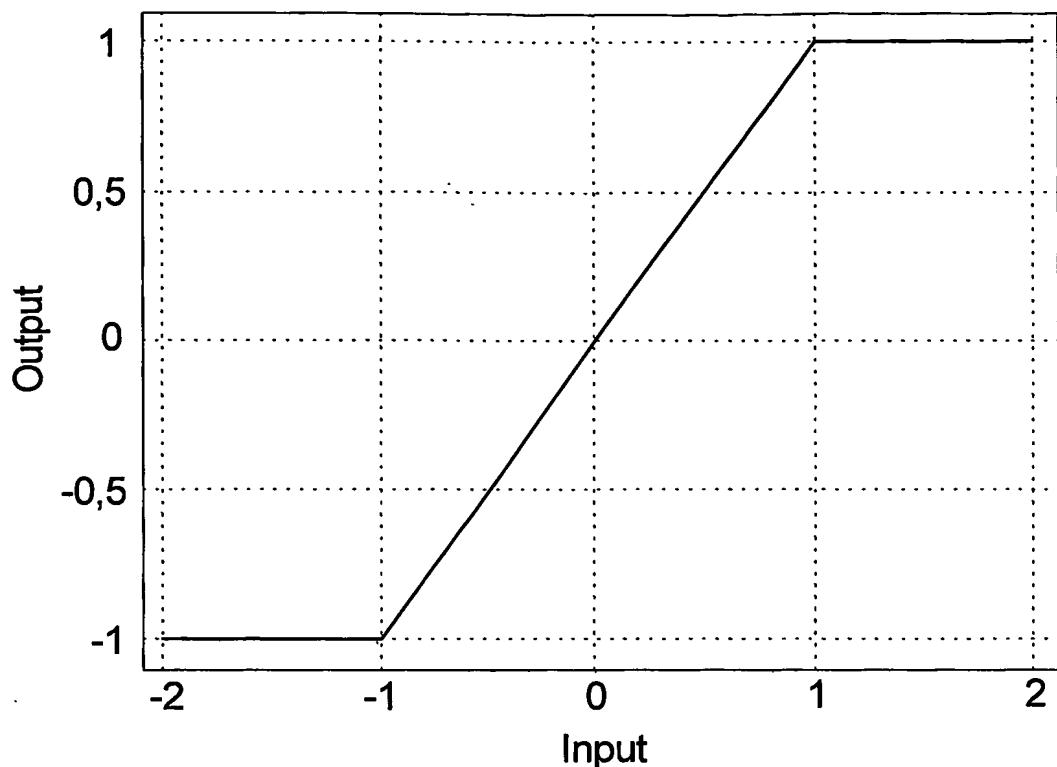


Fig. 22

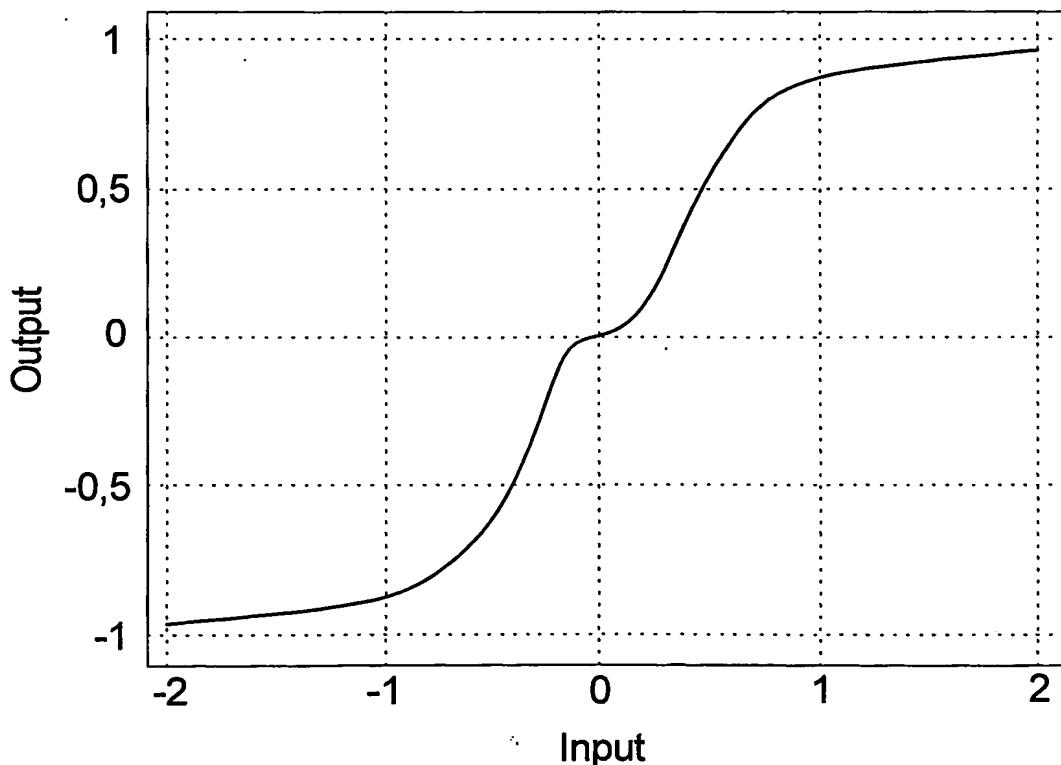


Fig. 23

17/27

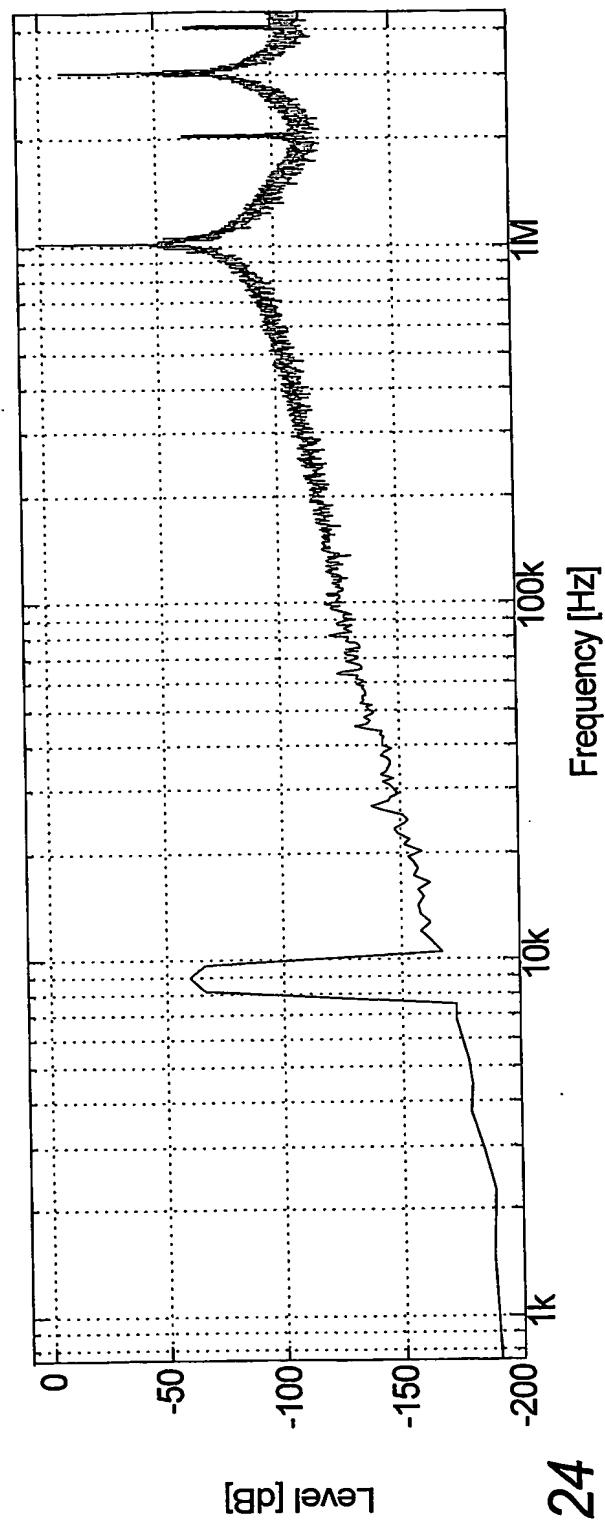
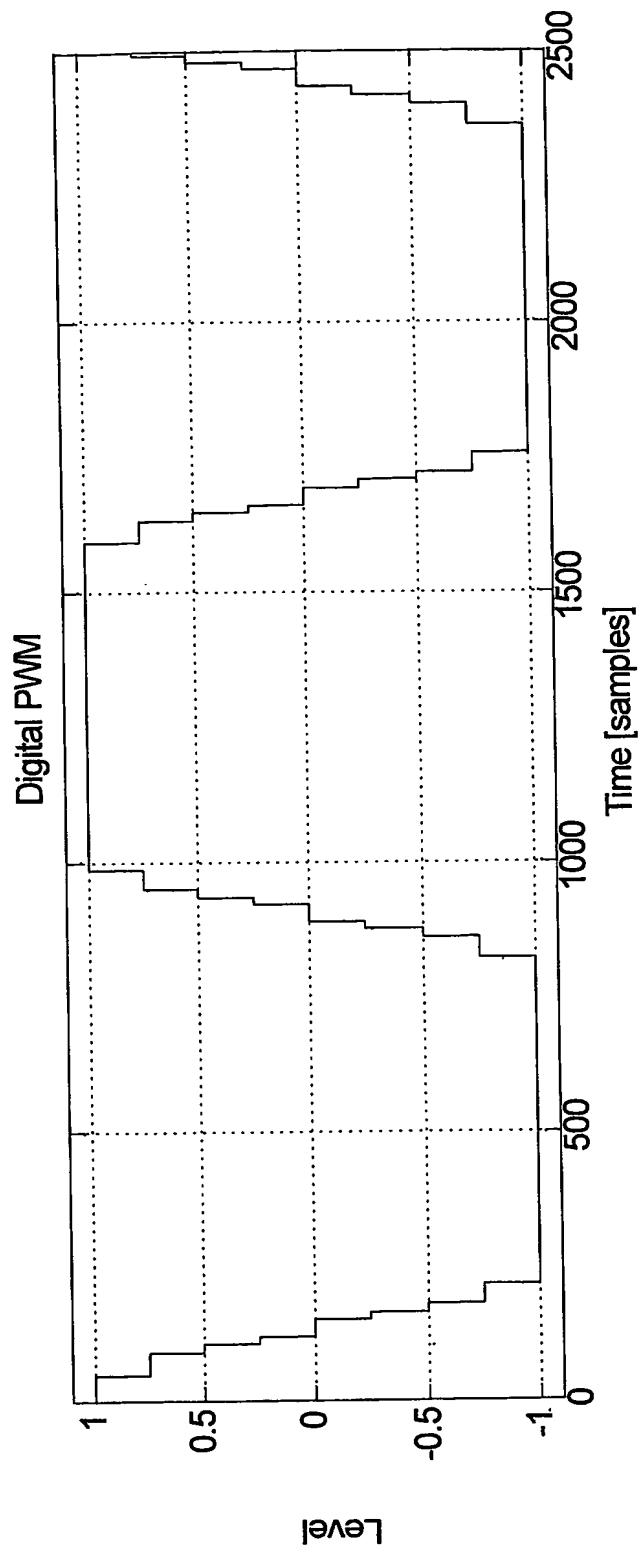


Fig. 24

18 / 27

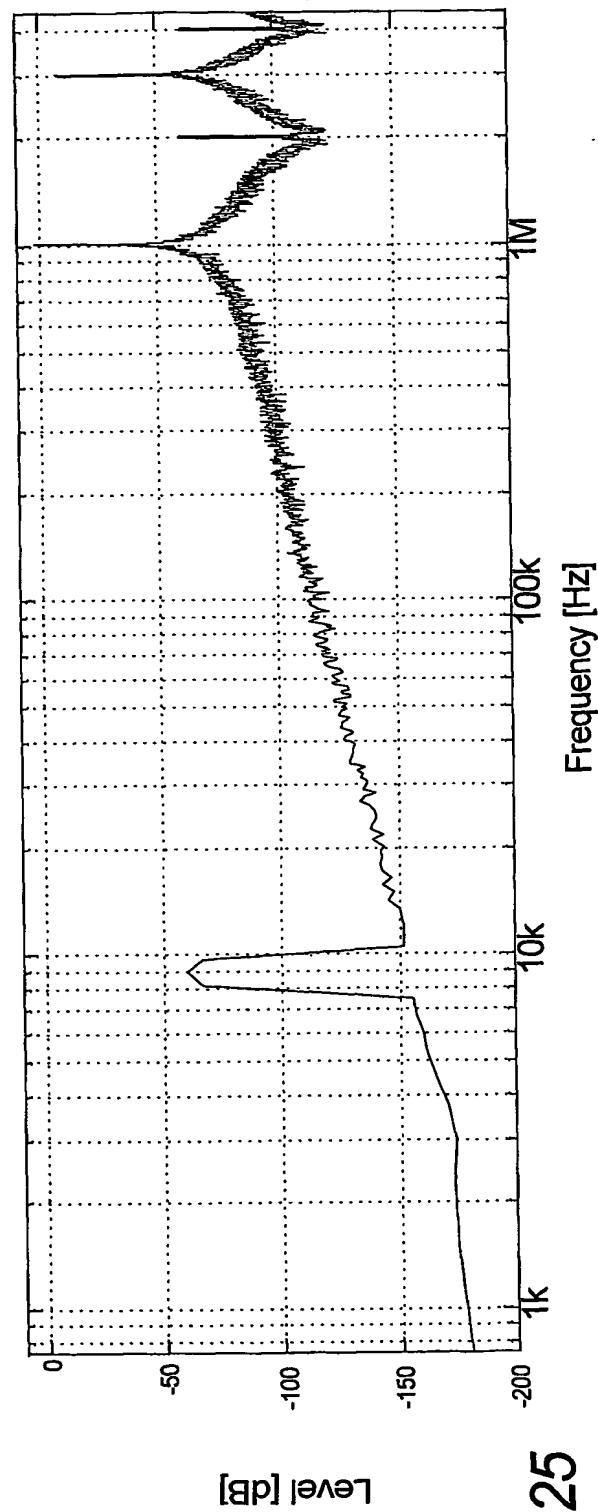
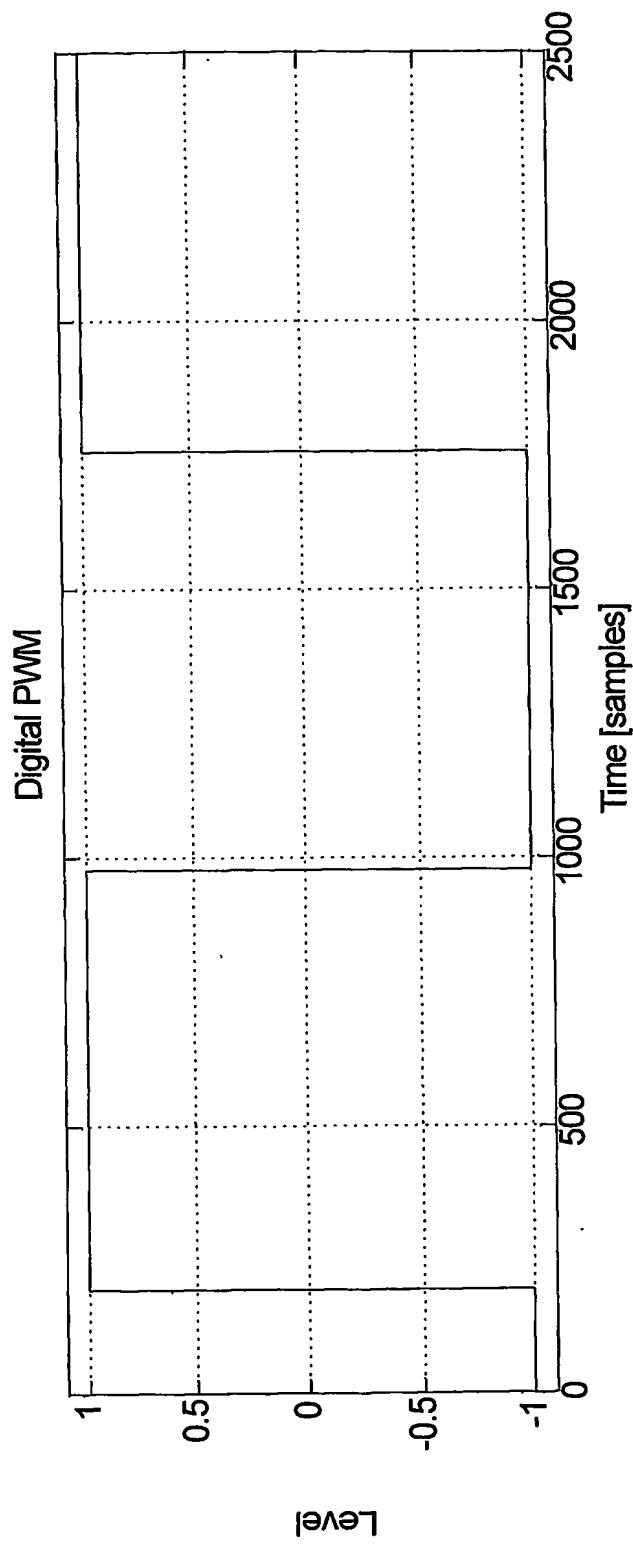


Fig. 25

19 / 27

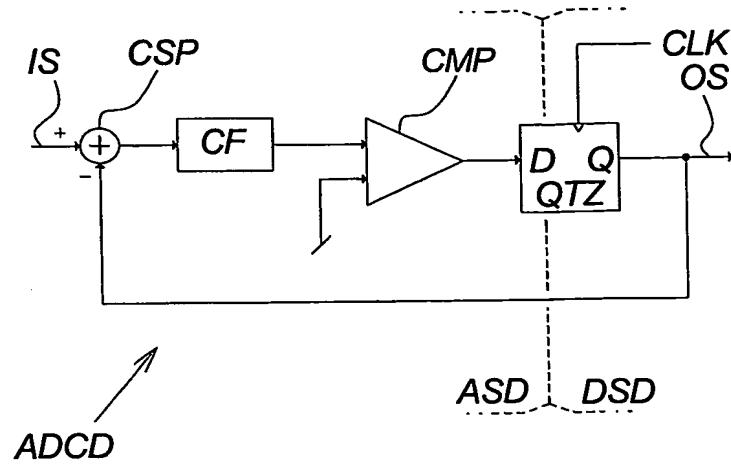


Fig. 26A

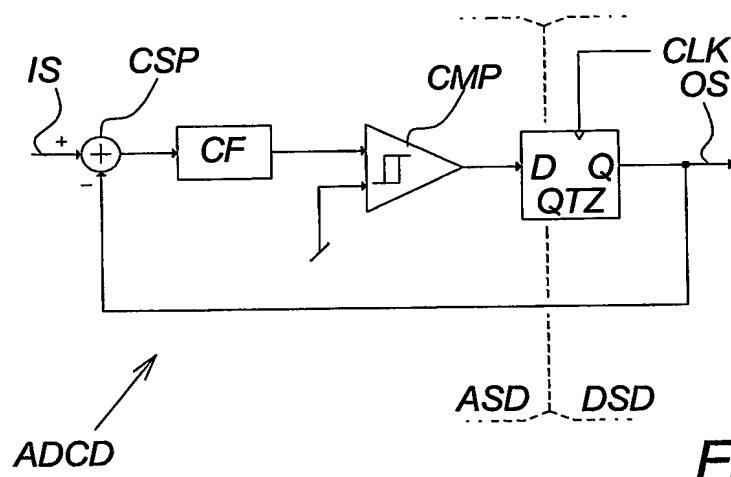


Fig. 26B

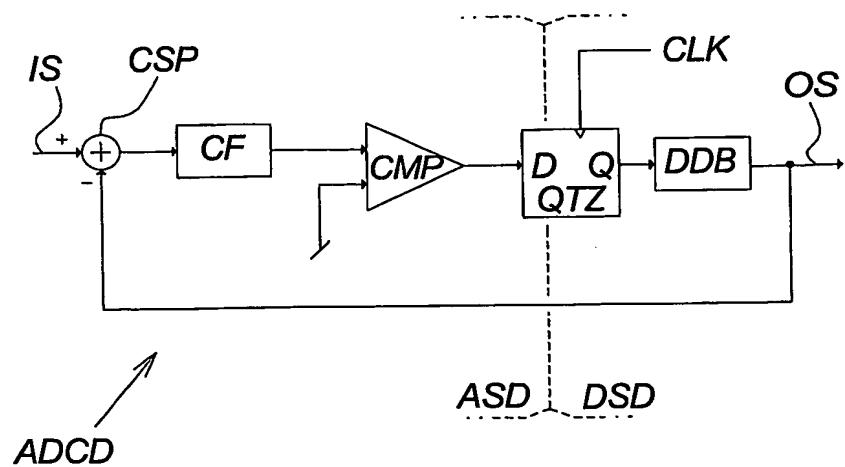
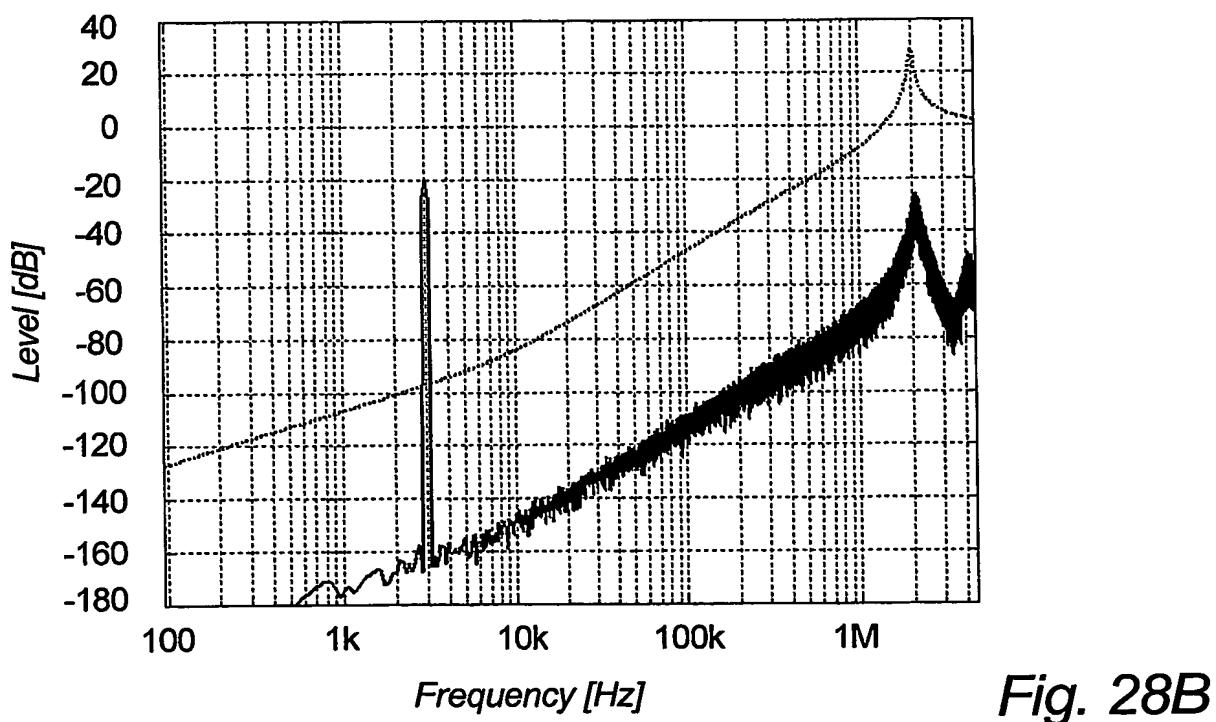
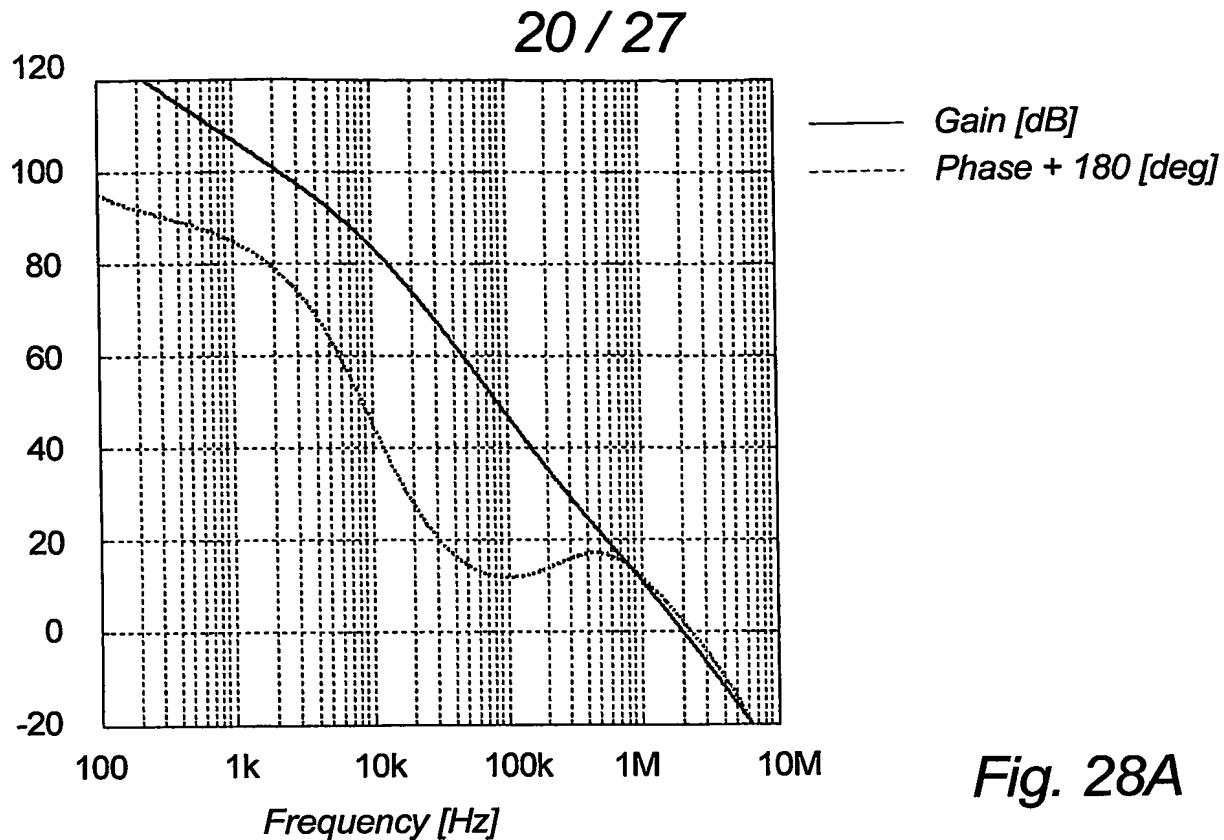


Fig. 26C



21 / 27

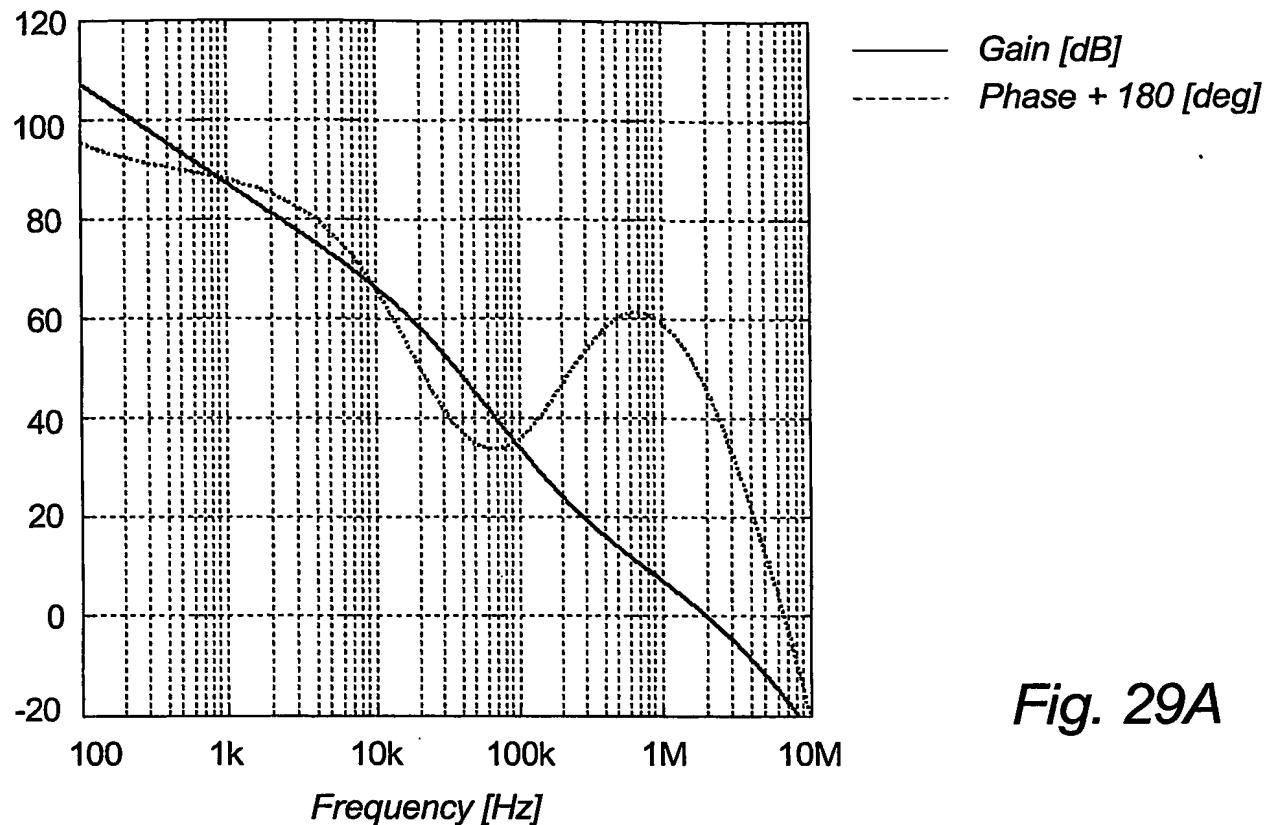


Fig. 29A

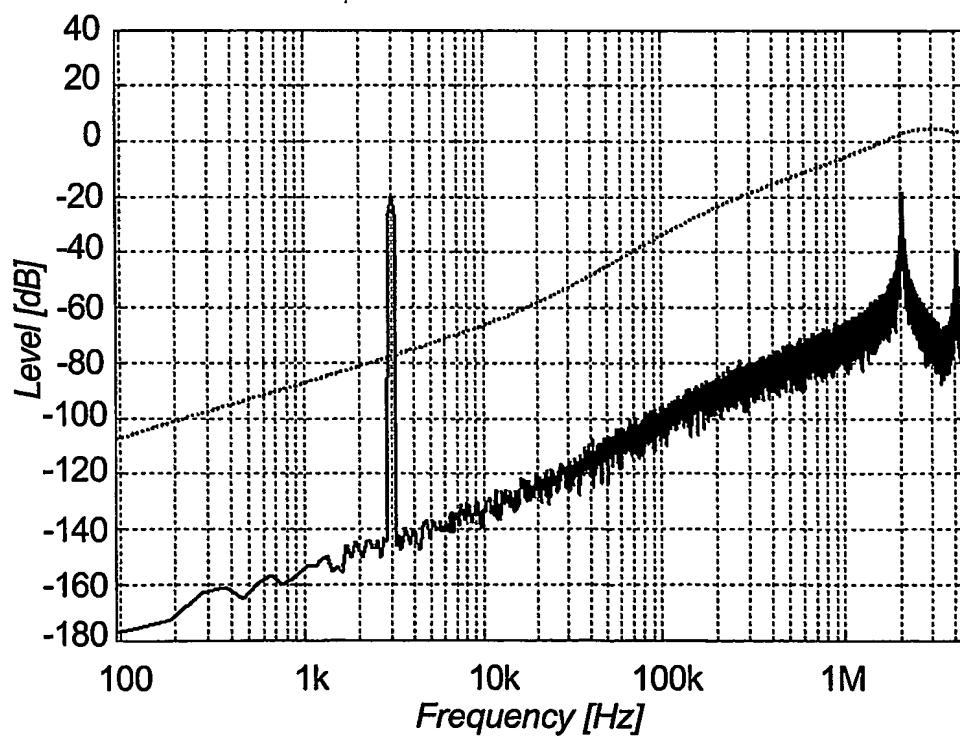


Fig. 29B

22 / 27

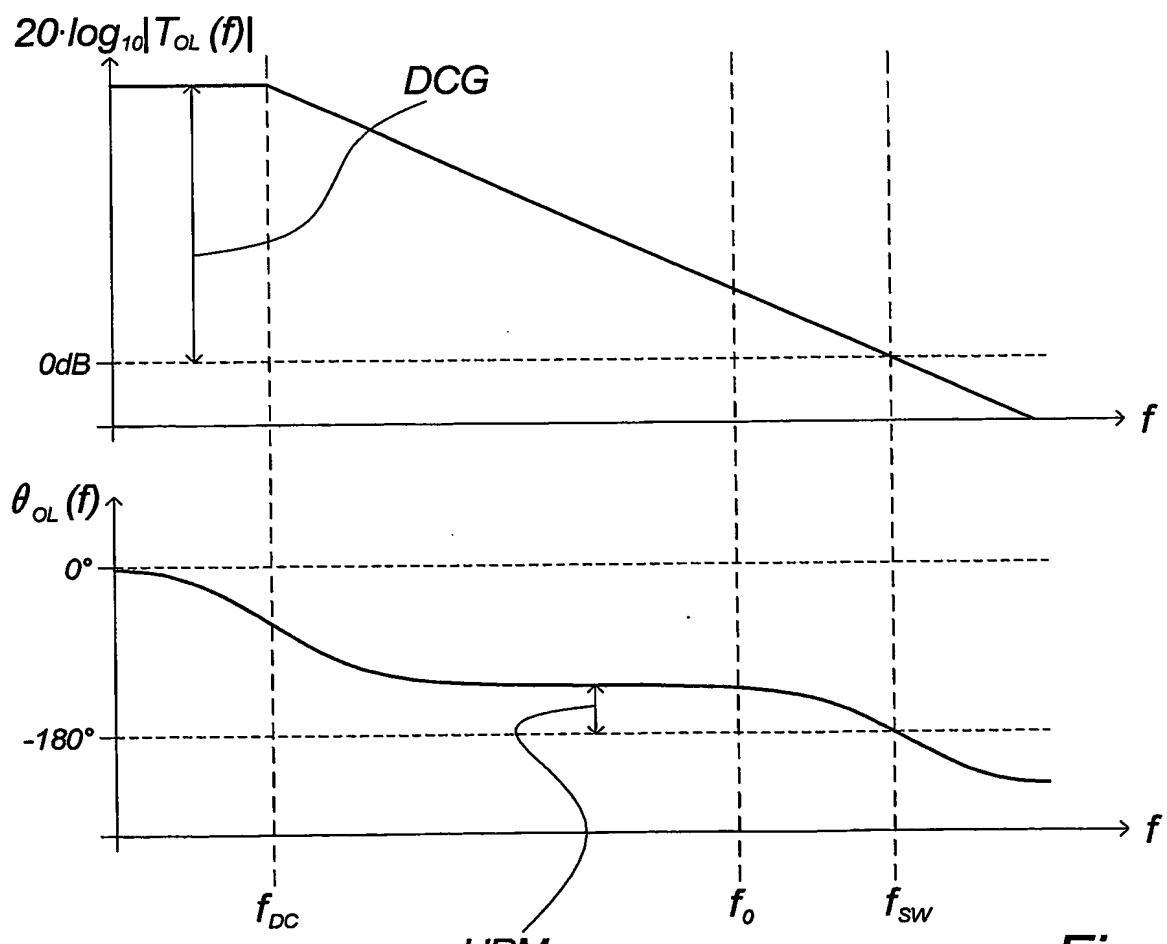


Fig. 30

23 / 27

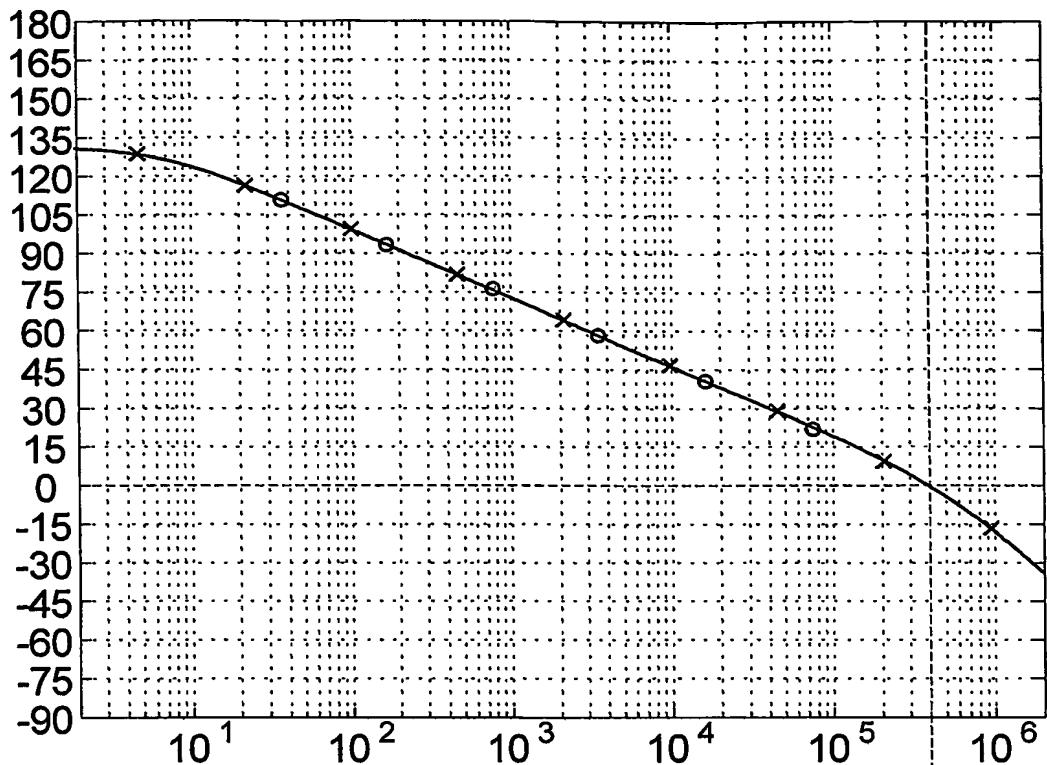


Fig. 31A

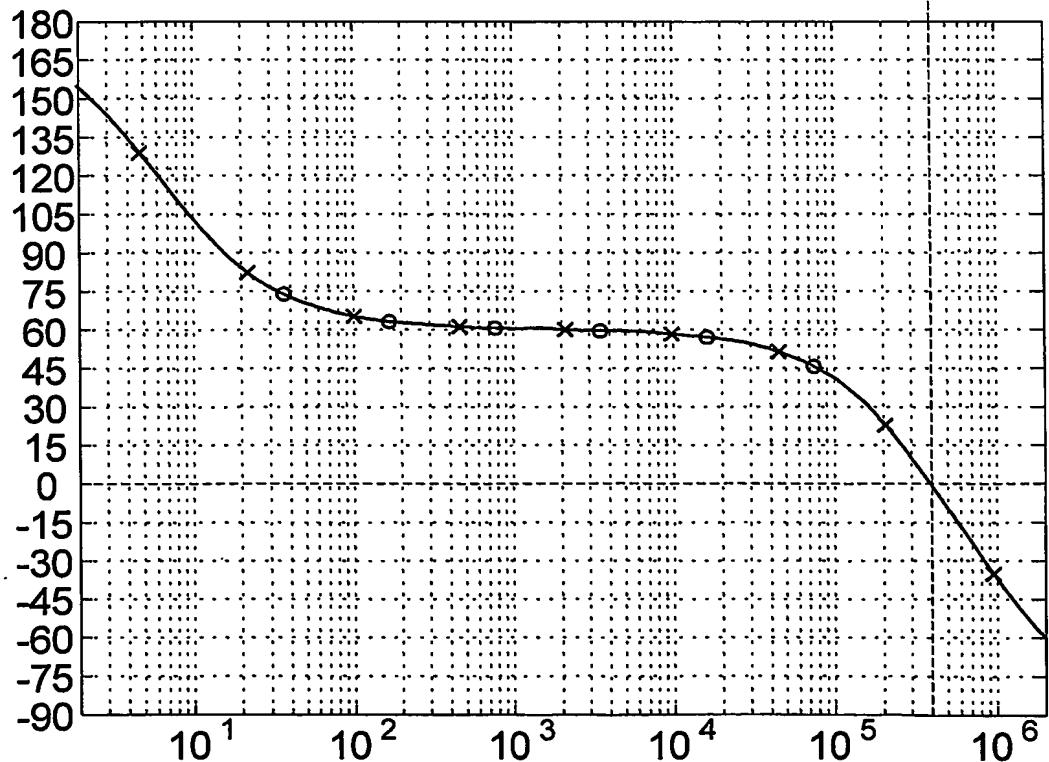


Fig. 31B

24 / 27

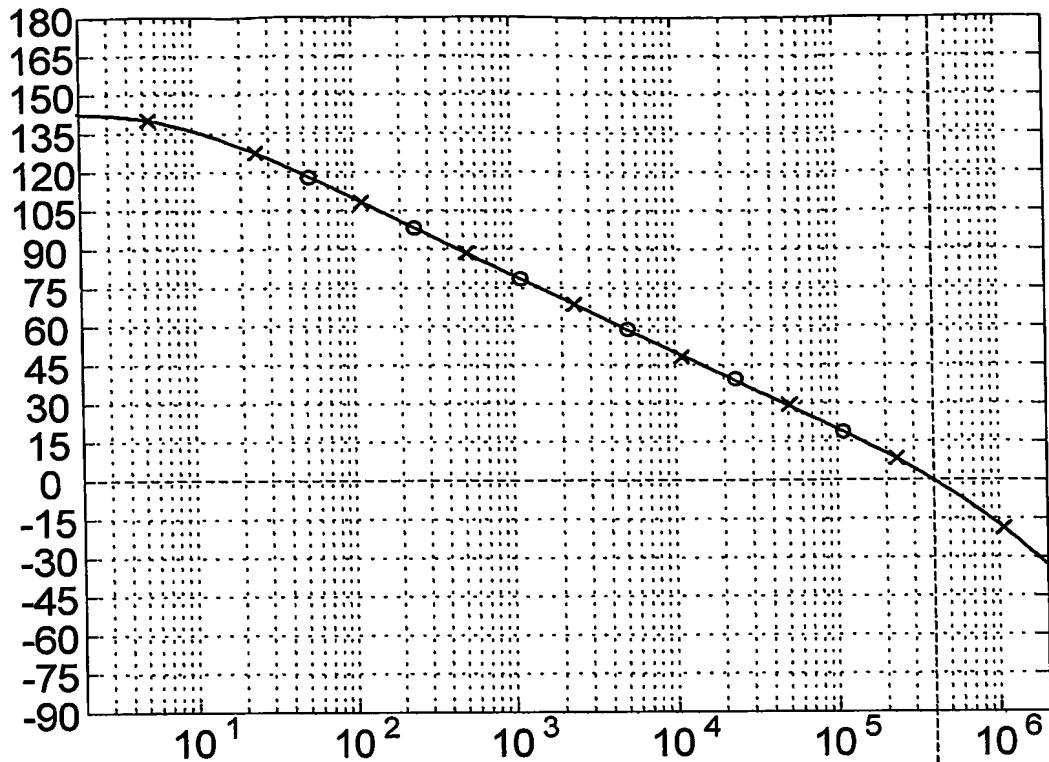


Fig. 32A

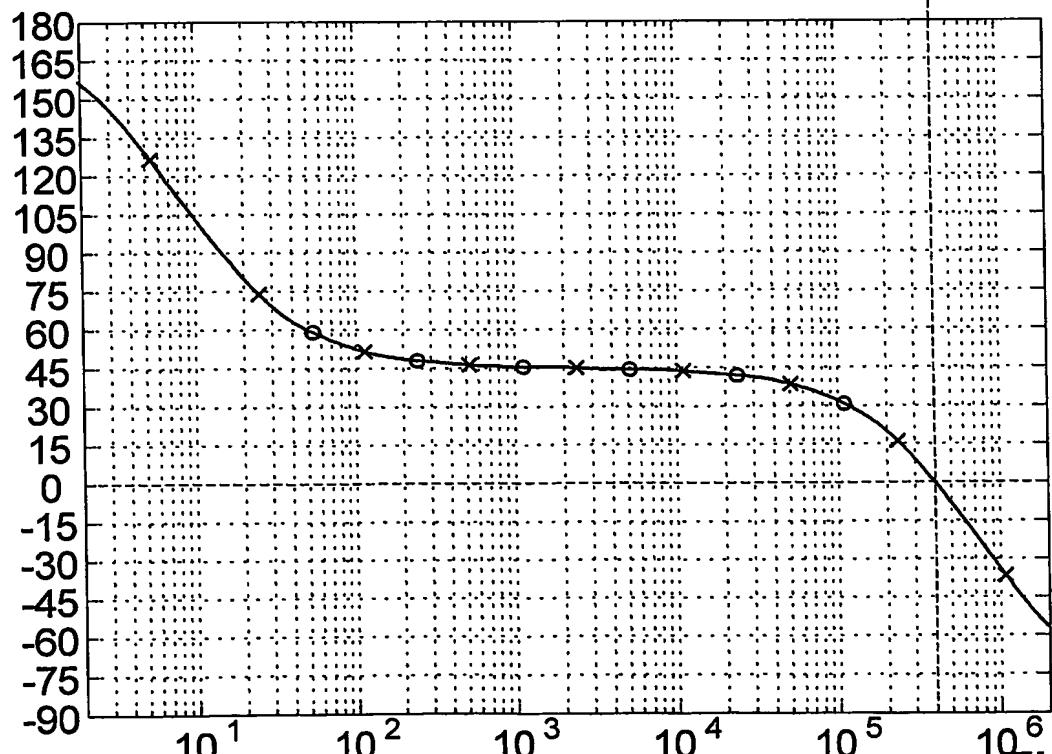


Fig. 32B

25 / 27

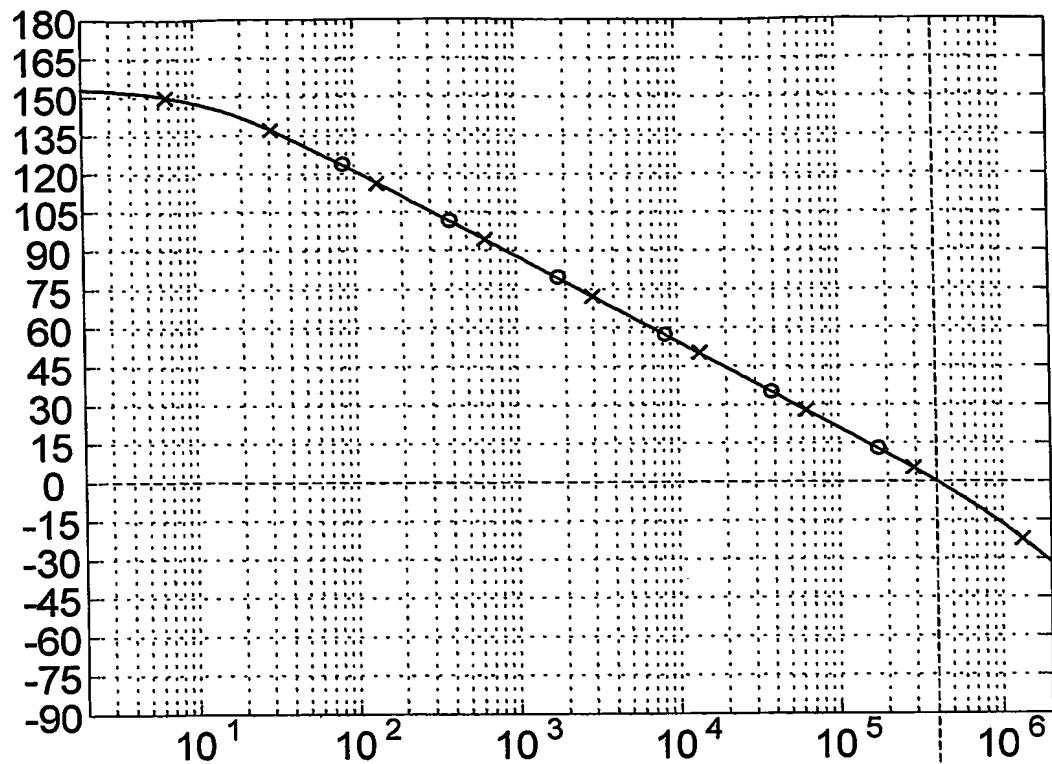


Fig. 33A

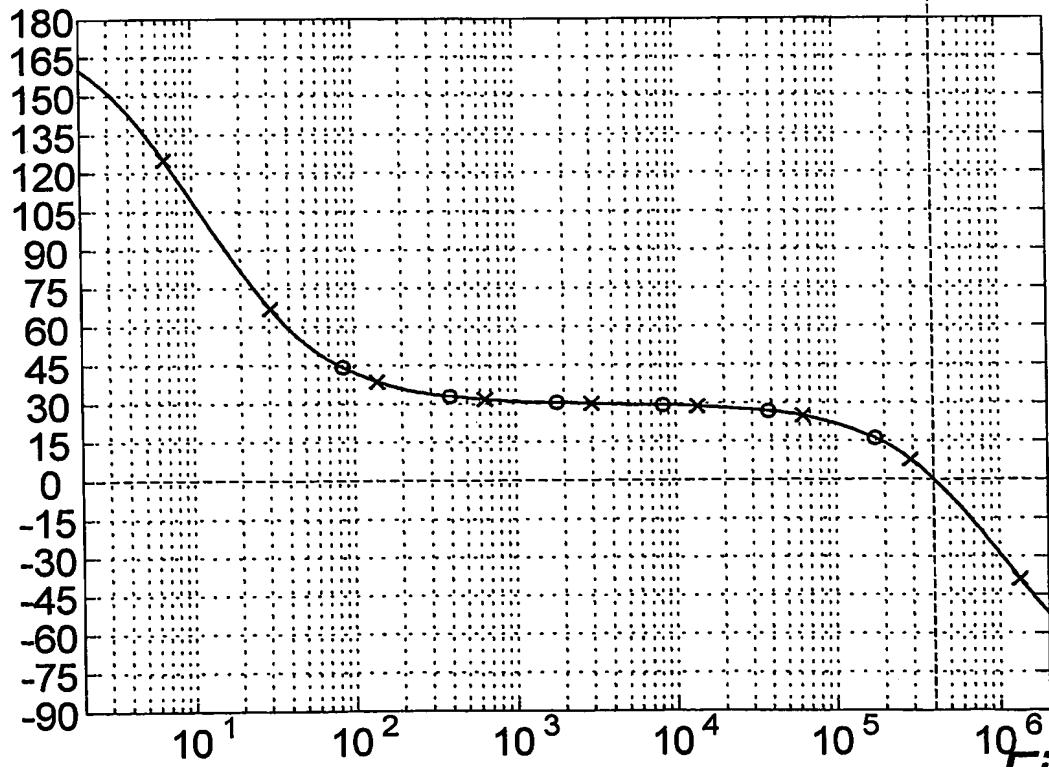


Fig. 33B

26 / 27

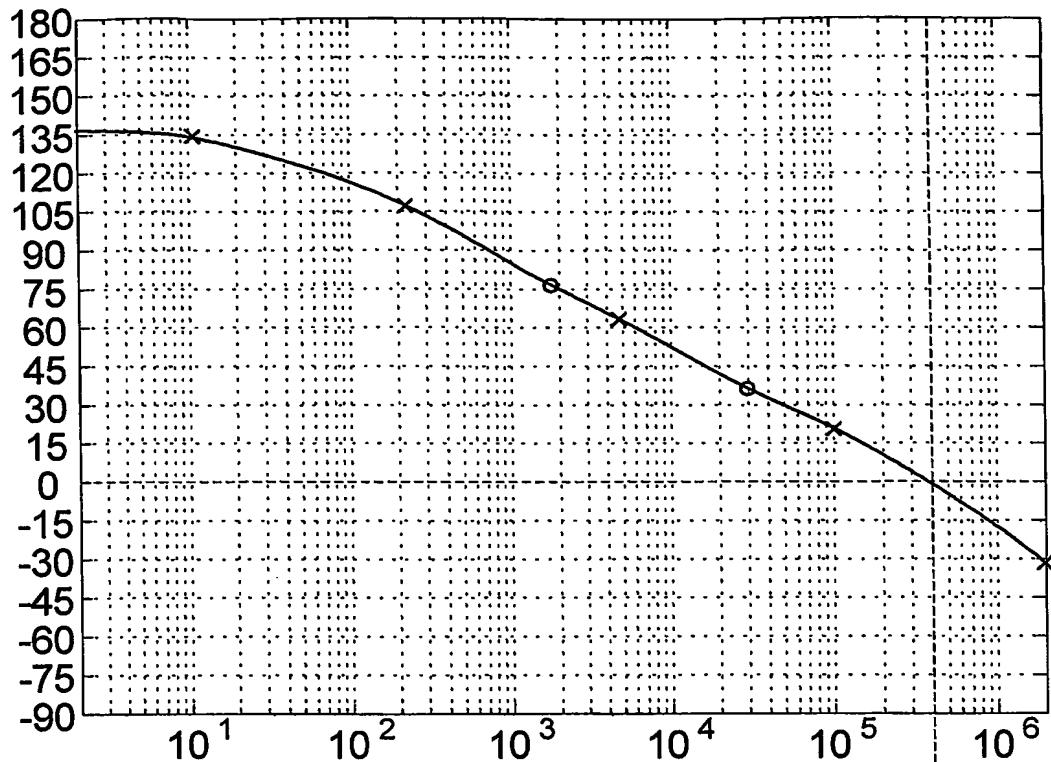


Fig. 34A

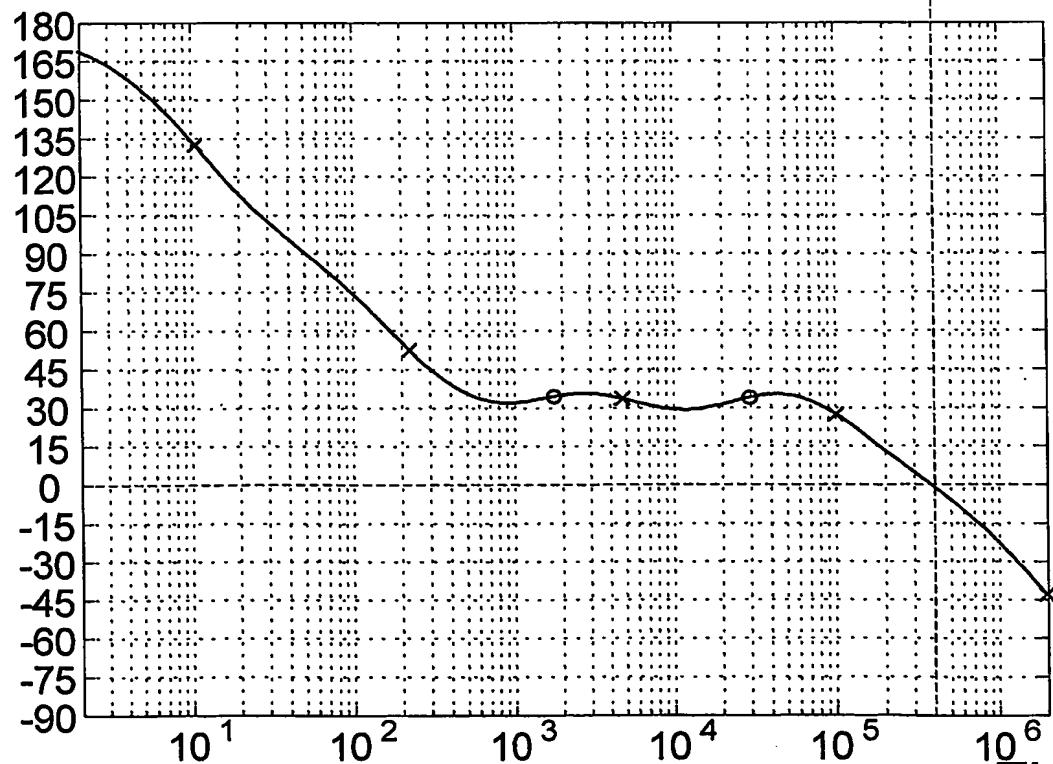


Fig. 34B

27 / 27

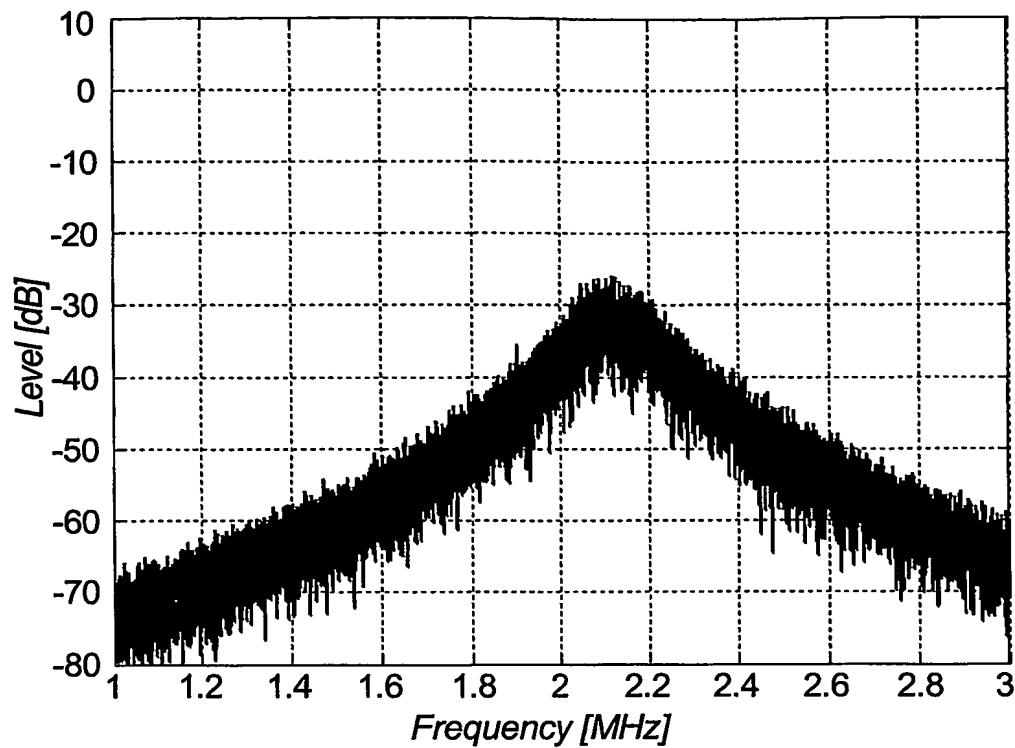


Fig. 35A

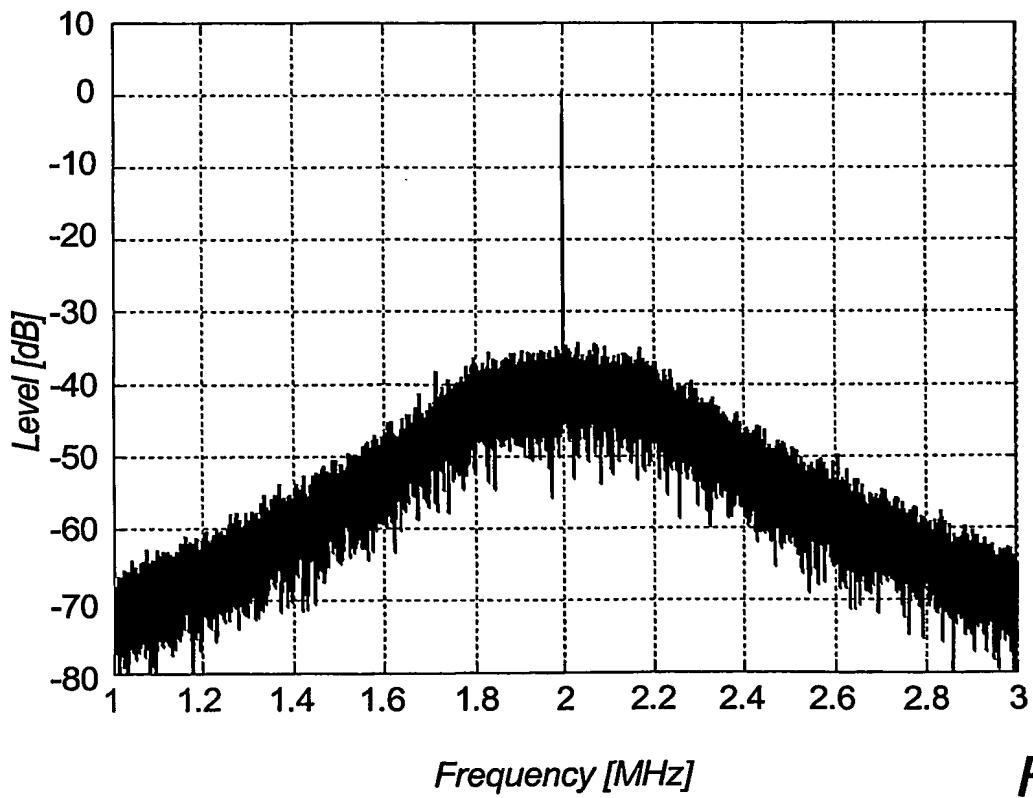


Fig. 35B